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SHUTTLE

TASK JSC/TRW 542

USER'S MANUAL FOR THE SHUTTLE ELECTRIC POWER SYSTEM ANALYSIS COMPUTER PROGRAM (SEPS)

VOLUME II OF PROGRAM DOCUMENTATION

JUNE 1974

Prepared For MISSION PLANNING AND ANALYSIS DIVISION NATIONAL AERONAUTICS AND SPACE ADMINISTRATION JOHNSON SPACE CENTER HOUSTON, TEXAS NAS 9-13834

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ABSTRACT

The Shuttle Electric Power System Analysis Computer Program (SEPS) was developed by TRW under JSC/TRW Task 542 for the Consumables Analysis Section of the Mission Planning and Analysis Division. The SEPS program has two major uses; first, to perform detailed load analysis including predicting energy demands and consumables requirements when the Shuttle electric power system is operated and perturbed in accordance with premission flight plans; and second, to perform parametric and special case studies on the Shuttle electric power system. As an additional feature, the SEPS program can be and has been used to analyze the ASTP Apollo electric power system. No program changes are required to use the SEPS program for analysis of the ASTP Apollo electric power system.

The SEPS Computer Program is written in FORTRAN V for use on the UNIVAC 1108 under the EXEC II operating system.

Documentation of the SEPS program is divided into two separate volumes:

VOLUME I - PROGRAM MANUAL

Contains descriptions of each major subroutine of the SEPS program including functional flow diagrams.

VOLUME II - USER'S MANUAL

Contained herein.

TABLE OF CONTENTS

Sect	ion	<u>Pag</u>
	ABST	RACT
	TABL	E OF CONTENTS
	LIST	OF TABLES
	LIST	OF FIGURES
1.0	INTR	ODUCTION
2.0	SEPS	PHASE I
	2.1	DISCUSSION
	2.2	DATA BASE 5
		2.2.1 Component Data
		2.2.2 Procedure Data
		2.2.3 Activity Data
		2.2.4 Switch Data
		2.2.5 Cyclic Data
	2.3	TIMELINE
		2.3.1 Source
		2.3.2 Construction
		2.3.3 Utilization
	2.4	FORMATTED PRINTOUT DESCRIPTION
	2.5	PLOT TAPE
	2.6	PHASE I INTERFACE TAPE
3.0	SEPS P	HASE II
	3.7	DISCUSSION
	3.2	PHASE II INPUT DATA
	3.2.	Circuit Definition

TABLE OF CONTENTS (CONTINUED)

<u>Sect</u>	ion	<u>Page</u>									
		3.2.1.1 Circuit Elements , ,									
		3.2.1.2 Distribution Circuit Description 34									
		3.2.1.3 Simple Circuit									
		3.2.1.4 Circuit Input Data									
		3.2.2 Variable Data									
	3.3	CARD TIMELINE DATA INPUT									
	3.4	FORMATTED PRINTOUT DESCRIPTION									
٠		3.4.1 DC Distribution Network Status 58									
		3.4.2 Source Status and Cryogenic Usage									
		3.4.3 Constraint Analysis Printout 63									
		3.4.4 Phase II COMUSE Printout 63									
	3.5	PLOT TAPES									
	3.6	PHASE II INTERFACE TAPE									
4.0	PHAS	PHASE I/PHASE II RUN PROCEDURES , , , . , .									
	4.1	PROGRAM OPTION CARDS , , , , , ,									
	4.2	PHASE I DECK SETUP									
		4.2.1 Tape Assignments									
		4.2.2 CUR									
		4.2.3 WLCCIT									
		4.2.4 JVMMPS									
		4.2.5 SEPS									
	4.3	DECK SETUP FOR PHASE II RUN AND PHASE I/PHASE II RUNS 76									
		4.3.1 Tape Assignments									
		4.3.2 CUR									
		4.3.3 WLCCIT									

TABLE OF CONTENTS (CONTINUED)

Section																												Page
4.3.4		JVN	1MP	'\$	•		•	•	•	•	•	•	•	۰	•	•	•		•	•	•	•	•	•	•	•	•	81
4.3.5		SEF	PS		•		•	•	•			•	•	¢		•	•	•	•		•			•	•	•	•	18
APPENDIX A			•		•	•	•		•			•	•		•	•			۰	•	•	•	•	•		•	•	A-1
APPENDIX B		•	•	•	•	•	•	•	•		•	•	•	۰	•		•	•	•			•	•	•	•	•	•	B-1
APPENDIX C	•	•		•	•	•	•		•	•	•	•			•	•	•	•	•	•		•	•	•	•		•	C-1
APPENDIX D	•	•	•	•	•	•	•	•	•		•,	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	D-1
APPENDIX E		•	•	•		•	•	•	•		•	•		•	•	•			•	•			•	•	•	•	•	E-1
APPENDIX F	•	•		•		•		•	•		•	•	٠		•	•			•	•	•	•	•		•	•	•	F-1
ADDENNIY C																												G_1

LIST OF TABLES

Table No.		Page
2-1	COMPONENT DEFINITION CARD	6
2-2	PROCEDURE/ACTIVITY ELEMENT DEFINITION CARDS	9
2-3	TIMELINE CARD FORMAT	14
2-4	CYCLIC TIMELINE CARDS	15
2-5	EXAMPLE OF MISSION PHASE ACTIVITY	16
2-6	LOAD SUMMARY TABLE	18
2-7	ACTIVE COMPONENTS AT THE END OF MISSION PHASE	19
2-8	POWER VERSUS TIME GRAPH	20
2-9	ACTIVE COMPONENTS UPON COMPLETION OF MISSION	22
2-10	COMPONENT ACTIVATION LIST	23
2-11	SUBSYSTEM/MISSION PHASE ANALYSIS	24
2-12	COMPONENT USAGE LISTED BY DECREASING PERCENT OF ENERGY USED	25
2-13	SUBSYSTEM USAGE SUMMARY	26
2-14	SUBSYSTEM SUMMARY BY MISSION PHASE	27
2-15	SEPS PHASE 1 PLOT TAPE	28
3-1	INPUT DATA VARIABLES FOR PHSE II	46
3-2	PLOT PARAMETERS	65
3-3	ASTP PLOT PARAMETERS	66
4-1	OPTION CARD FORMAT	68
4-2	UNITS CARD FORMAT	71
4-3	ABORT CARD	73
4-4	MISSION PHASE CARD(S)	77

LIST OF FIGURES

Fig	ure No.		Page
	1-7	FUNCTIONAL FLOW DIAGRAM	2
	3-1	INPUT/OUTPUT FLOW DIAGRAM OF SEPS PHASE II	32
	3-2	SIMPLE CIRCUIT	35
	3-3	BRANCH/NODE/ELEMENT CONFIGURATION OF SIMPLE CIRCUIT	37
	3-4	CIRCUIT DEFINITION DECK SETUP	38
	3-5	CARD DATA (FREE FIELD FORMAT)	39
	3-6	INPUT CARD LISTING - EXAMPLE	41
	3-7	INITIALIZATION DECK SETUP	56
	3-8	INITIALIZATION DATA CARD LISTING - EXAMPLE	57
	3-9	CARD TIMELINE DECK SETUP	59
	3-10	CARD TIMELINE DATA LISTING - EXAMPLE	60
	3-11	DC DISTRIBUTION NETWORK STATUS	61
	3-12	SOURCE AND INVERTER STATUS, AND CRYOGENIC USAGE	62
	3-13	CONSTRAINT ANALYSIS PRINTOUT	64
	4-1	OPTION CARD LISTING - EXAMPLE	74
	4-2	COMPLETE DECK SETUP FOR PHASE II OR PHASE II WITH PHASE I INTERFACE TAPE RUN	78
	4-3	LISTING OF TAPE ASSIGNMENT, CUR OPER., AND XQT SEPS CARDS	82
•	4-4	BUILD NEW DATA FILE WITH CIRCUIT DESCRIPTION INPUT	83

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1.0 INTRODUCTION

1.0 INTRODUCTION

This User's Manual defines how to use the SEPS Program.

Figure 1-1 depicts the functional flow diagram of the SEPS Program. SEPS consists of two major parts referred to as Phase I and Phase II. Phase I converts a mission event timeline into an electrical load profile and Phase II provides distribution circuit and power source operating points as a function of electrical loading and equipment parameters.

Section 2 defines SEPS Phase I data base requirements and formats, procedure and activity definitions, and mission timeline input formats.

Section 3 defines SEPS Phase II distribution circuit input and fixed data requirements.

Section 4 defines run procedures and deck setups for Phase I, Phase II, and combined Phase I/Phase II runs.

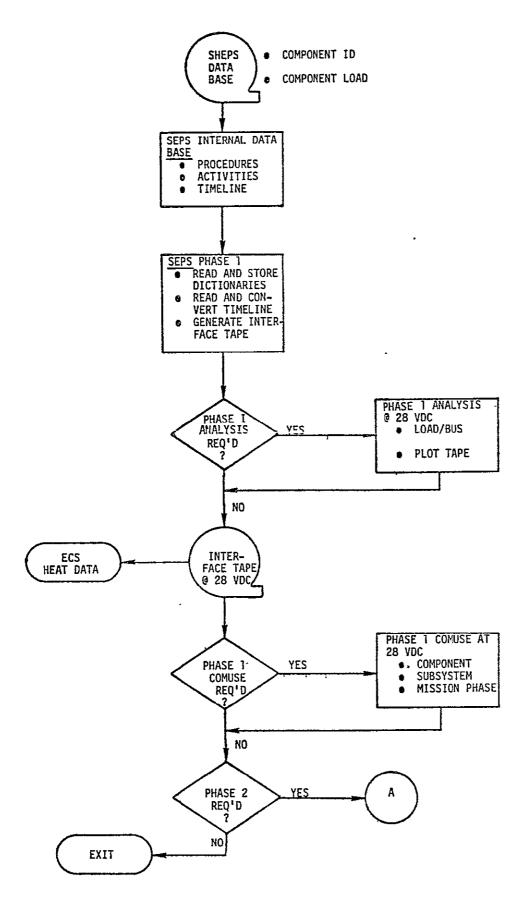


FIGURE 1-1. FUNCTIONAL FLOW DIAGRAM

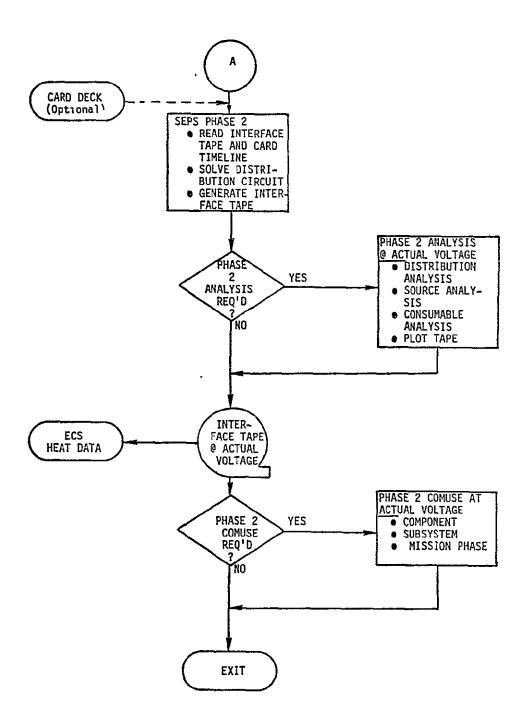


FIGURE 1-1. FUNCTIONAL FLOW DIAGRAM (CONTINUED)



2.0 SEPS PHASE I

2.1 DISCUSSION

The SEPS Computer Program Phase I converts a mission event timeline to an electrical load profile and provides subsystem and mission analyses of the power and energy demands for Shuttle missions. The analysis is based on a 28 VDC load bus voltage. The program utilizes a data base which describes all Shuttle electrical power consuming equipment in terms of power requirements and relating all the equipment to subsystems. This data combined with a desired mission event timeline provides the basis for the output interface tape consisting of event time point data and a listing of the activated components. The interface tape is utilized as the input driver for the Phase I COMUSE analyses and Phase II. The Phase I COMUSE analyses include the end of mission component, subsystem, and mission phase loads and energy summaries. The Phase I COMUSE analyses are discussed in Section 2.4 (FORMATTED PRINTOUT DESCRIPTION). Phase II utilization of the interface tape is discussed in Section 3.0 (PHASE I/PHASE II INTERFACE).

2.2 DATA BASE

2.2.1 Component Data

Data utilized to construct the component dictionary for the SEPS data base was obtained from the Master Electrical Equipment List furnished by Rockwell through JSC. The following characteristics of each component were used:

- ID# lst two digits: Subsystem ID
 2nd two digits: Rockwell ID
 3rd two digits: Component Mode
- 2. Component Title
- 3. AC or DC designation (if available)
- 4. Power value

These were combined with a load assignment. Then, to make the data complete, a power factor was assigned. These all combined make up the data for the component portion of the data base. Refer to Table 2-1 for the punched card format of this data.

TABLE 2-1. COMPONENT DEFINITION CARD

COMPONENT DEFINITION CARD (1 PER COMPONENT)

THE FORMAT OF THE COMPONENT CARDS IS AS FOLLOWS.

COLUMN	TYPE	DEFINITION
1	ALPHA	'C' INDICATES THE COMPONENT DEFINITION CARD
2	INTEGER	MODE
3	BLANK	NOT USED
4-9	INTEGER	COMPONENT ID NUMBER
10-11	INTEGER	LOAD NUMBER
12-13	BLANK	NOT USED
14-43	ALPHA	COMPONENT TITLE
44	BLANK	NOT USED
45-49	REAL	POWER FACTOR <1.
50-56	REAL	POWER CONSUMED @ 24. VDC
57-63	REAL	POWER CONSUMED @ 28. VDC
64-70	REAL	POWER CONSUMED @ 32. VDC
71-80	NOT USED	

The SEPS computer program has the capability to handle a maximum of 50 DC (or DC equivalent) load assignments and 9 inverters. The load numbers of 1 through 50 have been reserved for DC or DC equivalent loads. Load numbers 52 - 54 have been reserved for 3ϕ AC loads. Load numbers 61-69 have been reserved for 1ϕ AC loads.

The SEPS AC load number grouping is as follows:

Type of Load	Bus Assignments
3 & AC	AC #1
	AC #1
1 φ AC	AC #1
1 φ AC	AC #1
3 & AC	AC #2
	AC #2
1 å AC	AC #2
1 φ AC	AC #2
3 в AC	AC #3
	AC #3
1 à AC	AC #3
T φ AC	AC #3
	3 φ AC 1 φ AC 1 φ AC 3 φ AC 1 φ AC

The DC equivalent load for the AC components are specified by the assignment of a particular load number and branch location in the circuit definition. The circuit definition description is contained in Section 3.2.1.

2.2.2 Procedure Data

Procedures are collections of components grouped together for a specific function. For example, those components having to do with G&N at lift-off would be included in the same procedure.

The data required for a procedure is organized in the following manner. The procedure is assigned a unique number and title. The numbers are similar for procedures with similar functions; e.g.:

```
P110 G&N Baseline A (LO to LAND)
P111 G&N Baseline B (LO to LAND)
P120 NASA Comm
P121 NASA Comm
P125 USAF Comm
```

The corresponding component data follows the procedure number and title card. This data includes:

- 1. "Call" designation e.g., "CP" Component called from a procedure
- 2. Component Number
- 3. Start Time: component "start" time can be "delta"-ed to start either before* or after** the component is actually called from timeline.
- 4. Stop Time: same "delta" situation exists for stop time as for start time. Stop time cannot be 00000.
- 5. Use Factor: component is "ON" % of designated time period
- 6. Component Title
- 7. Procedure ID: solely convenience feature to designate which procedure a component belongs to when in card form.

Refer to Table 2-2 for procedure card format.

```
*NOTE 1: Signified by - sign preceding time delta on card **NOTE 2: Signified by + sign preceding time delta on card
```

TABLE 2-2. PROCEDURE/ACTIVITY ELEMENT DEFINITION CARDS (2 PER ELEMENT)
THE FORMATS OF THE PROCEDURE/ACTIVITY CARDS ARE AS FOLLOWS

CARD 1		
COLUMN	TYPE	DEFINITION
1	ALPHA	A INDICATES ACTIVITY DEFINITION P INDICATES PROCEDURE DEFINITION
2	BLANK	MUST BE BLANK
4-10	INTEGER	P/A ELEMENT NUMBER
12-47	ALPHA	TITLE (UP TO 36 CHARACTERS)
CARD 2	,	
1-2	ALPHA	CP - COMPONENT CALLED FROM A PROCEDURE CA - COMPONENT CALLED FROM AN ACTIVITY PA - PROCEDURE CALLED FROM AN ACTIVITY SP - SWITCH CALLED FROM PROCEDURE SA - SWITCH CALLED FROM ACTIVITY
3		Y - (if cyclic card)
4-10	INTEGER	COMPONENT NUMBER IF COLUMN 1 = C PROCEDURE NUMBER IF COLUMN 1 = P IF COLUMNS 9 AND 10 = 00 THEN ALL COMPONENTS WHOSE FIRST 5 DIGITS AGREE WITH COLUMNS 4-8 WILL BE READ IN
11		MODE
12-17	INTEGER	START TIME IN HHHMM (PLUS OR MINUS)*

9

*NOTE: This designation explained in more detail in formal write-up section on Procedure/Activity Cards.

TABLE 2-2. PROCEDURE/ACTIVITY ELEMENT DEFINITION CARDS (CONTINUED)
THE FORMATS OF THE PROCEDURE/ACTIVITY CARDS ARE AS FOLLOWS

CARD 2 (CONTINUED)

COLUMN	TYPE	DEFINITION
18-23 24-28 29-34 35-40	INTERGER REAL R R	STOP TIME IN HHHMM (PLUS OR MINUS)* USE FACTOR (GE O. AND LE 100.) PERIOD DECIMAL FRACTION OF ON TIME Applicable only to cyclic data cards
41-80		GROUPS OF FOUR COLUMNS (LLLV)** FOR SWITCH. IF NOT SWITCH, TITLE IN THIS SPACE

*NOTE: Stop time not used if this is cyclic card.

**NOTE: LLL provide spaces for branch where switch is located. V is I if switch closed, O if

switch open.

2.2.3 Activity Data

Activities are collections of procedures and/or components grouped together by "mission phase" considerations. That is, procedures and components grouped together which must be on at a particular time for a specified duration to accomplish a mission objective. E.g.:

A 1000 Prelaunch to Landing - contains all those components and procedures which must be on for entire mission; provides base load.

The Activity data is organized by:

- 1. Unique number and title
- 2. Corresponding procedure and component data as in procedure definition.

Refer to Table 2-2 for activity card format.

2.2.4 Switch Data

Phase I has the capability of controlling Phase II electrical power analysis on different circuit configurations obtained through opening or closing switches in the different branches. For example, all loads can be fed by two fuel cells by closing some switches and opening others. The effect of this action on the fuel cell loading can then be analyzed in Phase II.

The following data is required:

- 1. Card Type "S" for switch
- Start Time when the switch condition should change if not when switch card is "turned on".
- Stop Time when the switch condition will change to its opposite state.
- 4. Switch Location branch number.
- 5. Switch Value 0 for open, 1 for closed.

Refer to Table 2-2 for switch card format.

2.2.5 Cyclic Data

Certain components will actually cycle on and off during a Shuttle mission. Phase I analysis has the capability of simulating this activity through the program. The following data appears on the cyclic cards:

- Cyclic Identifier "Y"
- 2. Type of cyclic action
- 3. + for begin cyclic action
- 4. for end cyclic action
- 5. Mode
- 6. Time
- 7. Period of cycle
- 8. Decimal fraction of period component is active
- 9. Title

The cyclic card format is shown in Table 2-2.

2.3 TIMELINE

2.3.1 Source

Data utilized to create the timeline is gathered from two main sources. First, JSC Mission description documentation provides the bulk of the timeline information. From this source it is possible to obtain mission events and durations. Also, timelines for burns and attitude changes are incorporated into the SEPS timeline information to provide more detailed usage data for specific subsystems and components. Second, data from JSC subsystem groups are utilized to construct specific requirements portions of the timeline. E.g., the correct heater and communications equipment usage is obtained by using data from this source.

2.3.2 Construction

As stated above, data from JSC documentation are utilized in forming a chronological sequence of mission events. The SEPS data base activities,

procedures, and components are turned "on" and "off" at the appropriate time according to the sequence. A "plus" sign signifies turn on of the desired entity and a "minus" sign signifies turn off.

The following data appears in the timeline:

- Timeline entity identifier tells whether an activity, procedure, or component is affected.
- 2. "+" or "-": turn on or off, respectively.
- Entity Number: number of activity, procedure or component affected.
- Time: MET at which operation occurs.
- 5. Title: Names activity, procedure or component affected.

Refer to Tables 2-3 and 2-4 for the card formats.

2.3.3 Utilization

Timeline events as constructed are further grouped to be used in larger time blocks. All turn on/off times are "delta"ed to some base time value determined from the mission descriptions. E.g.,

- 1. LO to Sortie all operations referenced to zero
- 2. Sortie to End Sortie all operations referenced to 51 hours (time at which sortie begins as determined from Mission 2A description).
- 3. End Sortie to Touchdown all operations referenced to 149 hours (time at which sortie ops end for Mission 2A).

These can easily be made for any Shuttle mission and can be used to analyze many different options. Refer to Table 2-5 for an example. A program, JVMMPS, to be described later, organizes these blocks to form a proper time sequenced timeline.

2.4 FORMATTED PRINTOUT DESCRIPTION

The formatted printouts available from SEPS Phase I are discussed in subsequent paragraphs and examples of each type of printed output are provided.

TABLE 2-3. TIMELINE CARD FORMAT

THE FORMAT OF THE TIMELINE CARDS IS AS FOLLOWS

COLUMN		PURPOSE
1	T	INDICATES THIS IS A TIMELINE CARD
2	A/P/C/S	INDICATES WHICH TYPE OF OPERATION TO PERFORM
3-9		+ BEGIN OPERATION
		- END OPERATION
		ACTIVITY/PROCEDURE/COMPONENT/SWITCH NUMBER
10		MODE
11-19		TIME IN HHHHHMMSS (PLUS OR MINUS)
20		SPECIAL OPERATION FLAG * ·
21-33		BLANK
34-69		TITLE

A TIMELINE CARD INITIATES ITS OPERATION AT ITS "START" TIME AND CONTINUES UNTIL ITS "STOP" TIME AT WHICH TIME THE ACTIVE COMPONENTS ARE TURNED "OFF".

*NOTE: If it is desired to turn a component or procedure off in an different way than it was turned on, Column 20 must contain a non zero integer.

TABLE 2-4. CYCLIC TIMELINE CARDS THE FORMAT OF CYCLIC TIMELINE CARDS IS AS FOLLOWS

COLUMN		PURPOSE
1	Υ	INDICATES THIS IS A CYCLIC CARD
2	A/P/C	INDICATES WHAT TYPE OF CYCLIC ACTION
3-9		+ BEGIN CYCLIC FUNCTION
		- END CYCLIC FUNCTION
		ACTIVITY/PROCEDURE/COMPONENT NUMBER
10		MODE
11-19		TIME (HHHHHMMSS)
20		SPECIAL OPERATION FLAG * ·
21-25		PERIOD OF THE CYCLE GIVEN IN DECIMAL HOURS
26-30		DECIMAL FRACTION OF THE PERIOD THE ACTION IS TO BE TAKEN
31-33		BLANK
34-69		TITLE

A CYCLIC CARD BEGINS ACTION AT ITS "START" TIME AND CONTINUES UNTIL THE CYCLIC COMPONENTS ARE TURNED "OFF". SEE DISCUSSION OF TIMELINE OPERATIONS.

*NOTE: Only if Col. 20 > 0, can components concerned be turned off outside of the procedure/activity through which they are turned on.

TABLE 2-5. EXAMPLE OF MISSION PHASE ACTIVITY

			MISSION 2A OPTION 1 (DT 5	1)
TP~	217	0000000	RCS 01 TO ENTRY	(OFF)
TP+	114	0140000	STARTRACKER ALIGN W RCS	(ON)
TP+	114	0380000	STARTRACKER ALIGN W RCS	(ON)
TP+	114	0620000	STARTRACKER ALIGN W RCS	(ON)
TP+	114	0860000	STARTRACKER ALIGN W RCS	(ON)
TP-	820	0980000	PAYLOAD OBSERVATION	(OFF)
TP+	217	0980000	RCS OI TO ENTRY	(NO)

Any of the formatted printouts can be selected or omitted for printing at the option of the user. The first six tables are outputs available from SEPS Phase I, the remaining tables are outputs from COMUSE which is driven by the interface tape.

PRINTED OUTPUT

OUTPUT PROVIDED AT EACH LOAD CHANGE

- 1. Load Summary (Table 2-6) The Load Summary output occurs whenever a component has cycled ON or cycled OFF. It prints the power level of each subsystem after the component has cycled and the power output required from each fuel cell. The following defines the various sections of the Load Summary output:
 - The cycled component (equipment) listed with its power definition
 - b) Subsystems that presently require power
 - c) Ac and dc total load requirements
 - d) Fuel cell power required

OUTPUT PROVIDED AT THE END OF EACH MISSION PHASE

 Active Components at End of Mission Phase (Table 2-7) - This output lists the components that are active at the end of the mission phase. It provides a vehicle electrical component configuration check for verification of subsystems and program status.

OUTPUT PROVIDED EVERY 24 HOURS OF MISSION TIME

1. <u>Power vs Time Graph (Table 2-8)</u> - The graph is printed every 24 hours of elapsed mission time and provides a plot of the electrical power required during that time period. This graph gives a pictorial view of the power levels and what times the peaks and valleys occur.

TABLE 2-6. LOAD SUMMARY TABLE

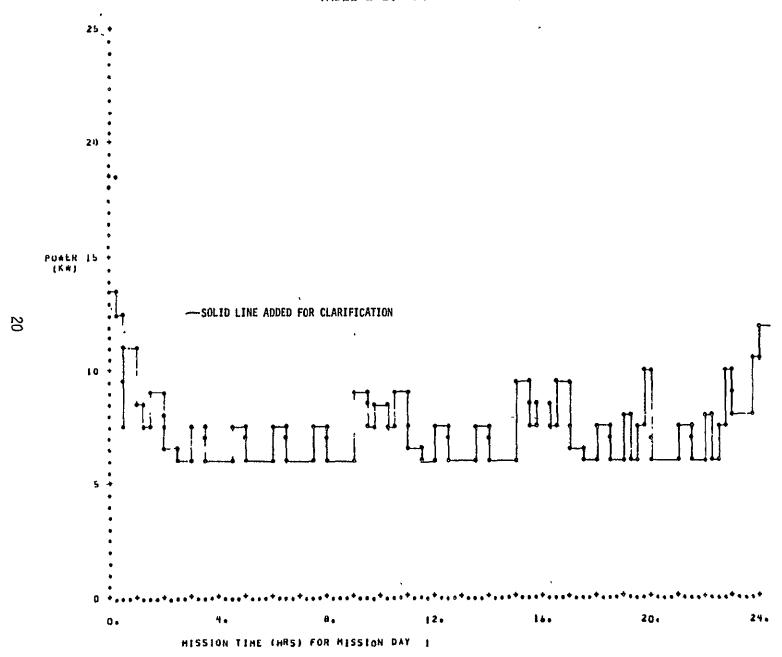
COMPO	AT TIME	32.9800 POINT 130 COMPONENT NAME	77 THE FOLLOWING	LOAD NO.	VE CHANGED COLDPLATE NO			D VALUE		PROCEDURE CALL	ED
	a 221502 221503 21101	TANK HTRS-FAD TANK HTRS-FAD TANK HTRS-FAD 5-BAND FH XMIT S-BAND FM SIGNAL	PROC	13gn. 23gg 810	SHITCHED SHITCHED SHITCHED SHITCHED SHITCHED	OFF OF ON		.0000 -	221500 221500 221500 21101 21201	0 0 0	32,9800 32,9800 32,9800 32,9833
	KEGOIKED OF	JTPUT AT TIME .	32.9800	دم			ACTIVITY. PRO	CEDURE OR CO	MPONENT	CALLED T	IME CALLED
b	SYSTEM 1 SYSTEM 5 SYSTEM 9 SYSTEM 21 SYSTEM 32 SYSTEM 52	911.4560 .0000 .0000 .0000 .0000	SYSTEM 2 SYSTEM 6 SYSTEM 15 SYSTEM 22 SYSTEM 40 SYSTEM 0	514.8000 556.7640 .0000 2.3296 2319.1999 .0000	SYSTEM SYSTEM SYSTEM SYSTEM SYSTEM SYSTEM	3 7 16 30 50	1017.00/6 1703.5179 .0000 403.7883 27.0400	SYSTEN SYSTEM SYSTEM SYSTEM SYSTEM SYSTEM	4 20 31 51 0	\$12.7200 .0000 .0000 265.2000 .0000	
c d	TOTAL AC LO	-	2317•386; 5916•436; 8233,822;	}							

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19

TABLE 2-7. ACTIVE COMPONENTS AT THE END OF MISSION PHASE

	•		•	•	LOSO MALMER	
	COMPONENT NO.	/ COMPONENT NAME	LOADING COLDPLATE NO.		LOAD VALUES	
	10131	THU .	405	100.00000	1.5,00000	ACTIVE COMPONENT
	10103	INU	•301	100.00000	198.60000 15.60000	VELIAL COMMONENA
	19701	STAR TRACKERS	40g l 40g t	100.00000	15.00000	ACTIVE CUMPONENT
	10777 10335	STAR TRACKERS STAR TRACKERS	90g i	100,00000	14.00000	ACTIVE COMPONENT
	10-01	BUDT HATE SENSOR PAG	4101	100.00000	5.20000	ACTIVE COMPONENT
	10402	HOUT HATE SENSOR PEG	4 102	100.00000	P+30000	ACTIVE CUMPONENT
	10=03	BOOT RATE SENSOR PEG	7103	100.00000	\$. 20000	ACTIVE COMPONENT
	10404	HODY RATE SENSON PKG	4101	100.00000	\$.2000o	ACTIVE COMPONENT
	10.05	HUDY RATE SENSON PKG	4102 *103	100.00000	5.2000Q 5.2000Q	ACTIVE COMPONENT
	10-04 11001	BUDY RATE SENSON PKG DRS EVC DRIVER UNIT	1101	100.00000	9.00000	ACTIVE COMPONENT
	11302	OHS THE DRIVER UNIT	2102	100.00006	7.00000	ACTIVE COMPONENT
	11003	OHS THE DRIVER UNIT	3103	100.00000	7.00000	. ACTIVE COMPONENT
	20601	AUDIO CENTER	5202	100.0000	\$0.0000	ACTIVE COMPONENT
	20.02	AUDIO CENTER	7202	100.00000	20.00000	ACTIVE COMPONENT
	20701	SIGNAL PROCESSOR	\$+501	100,00000	13,80000	ACTIVE COMPONENT
	10401	PAYLOAD DATA INTERLEAVER	19101	100.0000	20.00000	ACTIVE COMPONENT
	21201	S-BAND HULTIPLEXER	Zeigi 28102	100.00000	.4000g	ACTIVE COMPONENT
	21202	S-BAND MULTIPLEXER	28102	100.00000	15.00000	ACTIVE COMPONENT
	21703 1304	S-BAND XCUR (TORS) NEC 2 S-BAND XCUR (TORS) XMIT 2		100.00000	220.00000	ACTIVE COMPONENT
	21401	VHF+5-BAND AFER RE SES	26101	100.00000	8.40000	ACTIVE COMPONENT
	22301	USH XPOHO REC NASA	24201	100.00000	7.00000	ACTIVE COMPONENT
	2230Z	USB XPOND XHIT MASA	26201	100.00000	21.00000	ACTIVE COMPONENT
	22401	VOICE PROCESSOR	24101	100.00000	5,00000	ACTIVE COMPONENT
	22402	VOICE PROCESSOR	2#102 24101	100.0000	5.00000 0.00000	ACTIVE COMPONENT
	22531	DIGI COMM PRUCESSOR	24102	100.0000	40.00000	ACTIVE COMPONENT
	22502 22601	DIGI COMM PROCESSOR USB DECODER	26101	100.00000	12.00000	ACTIVE COMPONENT
00	22603	USB DECODER	28102	100.00000	12.00000	ACTIVE COMPONENT
ORIGINAL PAGE IS OF POOR QUALITY	30102	2 DR 3 AXIS ATT DIR IND	43G1	100.00000	25.00000	ACTIVE COMPONENT
# E	10416	ATY IND OMS/RCS	5301	100.00000	6.00000	ACTIVE COMPONENT
\sim $^{\rm H}$	71201	CAUT + WARNING UNIT	24301	100.00000	11.00000	ACTIVE COMPONENT
82	31401	PERF HON ELECT UNIT	273pl 43pl	100.0000	5.00000	ACTIVE COMPONENT
∌⊳	31701 31702	MISSION TIMERS MISSION TIMERS	4301	100.00000	5.00000	ACTIVE COMPONENT
🗖	31005	EVENT TIMERS	7101	100.00000	7.50000	ACTIVE COMPONENT
PAGE QUALL	31701	CRT DISPLAY UNITS	≜30 3	100.00000	40.00000	ACTIVE COMPONENT
	31702	CRT DISPLAY UNITS	7101	100+00000	40.00000	ACTIVE COMPONENT
ළිබ	35071	KEYBOARDS	43 <u>0</u> 1	100.00000	15.00000 15.00000	ACTIVE COMPONENT
	32002	KE TUOARDS	7301 25301	100,00000	120.00000	ACTIVE COMPONENT
	34161 32102	DISPLAY PRUCESSORS DISPLAY PROCESSORS	27301	100.00000	120.00000	ACTIVE COMPONENT
₹ 55	12202	DISPLAY CPLR DR UNITS	27301	100.00000	20.00000	ACTIVE COMPONENT
	32402	ROT HAND CONTR	4301	100.00000	3.56400	ACTIVE COMPONENT
	11002	CONTRL ENCOR/CPLR UNIT	29301	100.00000	25.00000	ACTIVE COMPONENT
	31101	FLT DECK FLOOD LIGHTS	7301	100.00000	1.50000	ACTIVE COMPONENT
	71305	FLT DECK FLOOD LIGHTS	5301 4301	100.00000	1.50000 1.50000	ACTIVE COMPONENT
	31303	FLT DECK FLOUD LIGHTS	7361	100.0000	1.50000	ACTIVE COMPONENT
	23104 2011	FLT DECK FLOUD LIGHTS FLT DECK FLUOD LIGHTS	4301	100.00000	1.50000	ACTIVE COMPONENT
	33309	FLT DECK FLOOD LIGHTS	7301	100,00000	1.50000	ACTIVE COMPONENT
	33307	FLT DECK FLOOD LIGHTS	4301	100,00000	1.50000	ACTIVE COMPONENT
	31709	FLT DECK FLOUD LIGHTS	4301	100.00000	1.50000	ACTIVE COMPONENT
	33401	FLT DECK FLOOD LTS	4301	100.00000	• 9000g	ACTIVE COMPONENT
	31402	FLT DECK FLOOD LTS	7301	100.00000	.9000	ACTIVE COMPONENT
			•	L DUTY CYCLE		



OUTPUT PROVIDED AT THE END OF THE MISSION

- 1. Active Components Upon Completion of Mission (Table 2-9) This output lists those components that are still active at the end of the mission. This provides a check on the timeline sequence used to verify that the Shuttle is in the proper deactivated configuration.
- Active Component Listing (Table 2-10) This output lists all
 components by subsystem and the number of times each component is
 activated.
- 3. <u>Subsystem/Mission Phase Analysis (Table 2-11)</u> This output compares each subsystem power demand to total vehicle power usage during each mission phase. The subsystems are listed with average power, maximum power and percent of total power.
- 4. Component Usage Listed by Decreasing Percent of Energy Used (Table 2-12)
 This output provides a power summary for all components, ranking them
 in decreasing order based on amount of energy consumed during the
 mission.
- 5. <u>Subsystem Usage Summary (Table 2-13)</u> This output lists all electrical components by subsystem with a power summary (power required, use factor, time activated, energy required) of each component.
- 6. <u>Subsystem Summary by Mission Phase (Table 2-14)</u> The summary lists each subsystem with its energy demand for all mission phases.
- Component/Time Input Listing SEPS provides echo capability of the inputs provided to the program.

2.5 PLOT TAPE

Phase I of the SEPS Program provides a plot tape of time (MET) versus various load parameters defined at 28 volts d-c. Table 2-15 defines the parameters included on the plot tape and their relative word location within each record.

	COMPUNEA	12 PHICH HAVE "OL	BEEN D	L-A	717	ATED AT	HISSLU	I END	
ORIGINAL' OF POOR Q	200201	HAIN ENGINE HEAT	ERS			13)		CONDITION	4
₹ ₹	\$00105	MAIN ENGINE HEAT	ERS			111	S#ITCH	COMPITION	4
7 H	200203	HAIN ENGINE HEAT	ERS			(3)	SWITCH	CONDITION	4
OH -	211001	THERMAL CONTHUL	HTRS			12)	SWITCH	COMPITION	d
22	211002	THERMAL CONTROL	HTRS			(2)	SHITCH	CONDITION	4
<i>₹</i> 7 <i>[</i> 2-	220701	CAT BED HIRS		5	ijΝ	[] 6]	SaltCH	CONDITION	1
ان ا	220702	CAT BED HTRS		5	gΝ	(16)	SWITCH	CONDITION	
7	220703	CAT BED HTRS		6	0 N	(16)	SHITCH	COMDITION	- 1
$\Delta \Delta$	220801	CAT BED HTRS		8	DN	1241	SWITCH	CONDITION	3
(F) (G)	220802	CAT BED HTRS		8	OΝ	1241	SALTCH	CONDITION	
u Page is Quality	220803	CAT BED HTRS		a	ON	(24)	SALICH	CONDITION	b
₹ 55	221101	TANK HTHS AFT				(4)	SHITCH	CONDITION	4
' ((2)	221102	TANK HTHS AFT				(4)	SWITCH	CONDITION	4
	221103	EANK HIRS AFT				(4)	SHITCH	COHDITION	4
	221104	TANK HTRS AFT				(4)	SWITCH	CONDITION	4
	501301	AUX HOTUR PUNPS	£ 2	ON)		143		CONDITION	1
	501302	AUX MOTON PUMPS		ONI		(4)		CONDITION	1
N	709104	WATER BOILER HTR		•		iHj		CONDITION	ž
22	501602	WATER BOILER HTR				(4)		CONDITION	۵
	501603	WATER BUILER HTR				(4)	SWITCH	CONDITION	2
	501604	MATER BOILER HTR				(4)		CONDITION	2
	400101	AFT AVIONICS HTR				iti		CONDITION .	2

TABLE 2-10. COMPONENT ACTIVATION LIST

manuali anno e	A	·	
	ACTIVATION LIST	THUMBER OF CALLS FROM TIMELIN	E
COMPONENT NO	THU COMPONENT NAME	TOTAL ACTIVATION COUNT 2	
(0102	140	10TAL ACTIVATION COUNT 1	
10183	inu	TOTAL ACTIVATION COUNT 11	
[030]	STAN THACKERS	TOTAL ACTIVATION COUNT 1	
16302	STAR THACKERS	TOTAL ACTIVATION COUNT ID	
10703	STAR TRACKERS	TOTAL ACTIVATION COUNT 1	
10601	BOOY RATE SENSUR PKG	TOTAL ACTIVATION COUNT 2	
(667)	BODY HATE SENSOR PKG	TOTAL ACTIVATION COUNT 2	
10003	HODY RATE SENSUR PKG	TOTAL ACTIVATION COUNT 2	
10901	BUDY RATE SLUSON PKG	TOTAL ACTIVATION COUNT 1	
10405	BODY RATE SENSON PKG	TOTAL ACTIVATION COUNT	
10000	BOUY HATE SENSOR PKG	TOTAL ACTIVATION COUNT 1	
16007	BUDY RATE SENSOR PKG	TOTAL ACTIVATION COUNT 11 TOTAL ACTIVATION LOURT 11	
10008	BODY RATE SENSOR PKG	, , , , , , , , , , , , , , , , , , ,	\circ
16609	BODY RATE SENSOR PRO	TOTAL ACTIVATION COUNT 11 TOTAL ACTIVATION COUNT 1	Q Q
10801	ACCEL PKG-NORMAL LATERAL ACCEL PKG-NORMAL LATERAL	TOTAL ACTIVATION COUNT &	
10803	ACCEL PRG-NORHAL LATERAL	TOTAL ACTIVATION COUNT 1	ORIGINAL OF POOR
	ACCEL PKG-NORHAL LATERAL	TOTAL ACTIVATION COUNT	8 2
6604 10605	ACCEL PKG-NORHAL LATERAL	TOTAL ACTIVATION COUNT I	
10909	ACCEL PKG-NORHAL LATERAL	TOTAL ACTIVATION COUNT	100 E3
10807	ACCEL PKG-NORMAL LATERAL	TOTAL ACTIVATION COUNT 1	QUALITY
10808	ACCEL PKG-NORHAL LATERAL	TOTAL ACTIVATION COUNT 1	\overline{A}
10404	ACCEL PKG-NORHAL LATERAL	TOTAL ACTIVATION COUNT L	
10810	ACCEL PKG-NORMAL LATERAL	TOTAL ACTIVATION COUNT !	
10811	ACCEL PEG-HORMAL LATERAL	TOTAL ACTIVATION COUNT	屋
10812	ACCEL PKG-NORHAL LATERAL	TOTAL ACTIVATION COUNT 1	
10401	ANGLE OF ATTACK PROBE	TOTAL ACTIVATION COUNT	
10902	ANGLE OF ATTACK PROBE	TOTAL ACTIVATION COUNT	
10903	ANGLE OF ATTACK PROBE	TOTAL ACTIVATION COUNT 1	
11001	OHS TYC DRIVER UNIT	TOTAL ACTIVATION COUNT 10	
11002	DHS TVC DRIVER UNIT	TOTAL ACTIVATION COUNT 1	
11003	OHS TVC DRIVER UNIT	TOTAL ACTIVATION COUNT 1	
11101	APS DRIVER/MONITOR APS DRIVER/MONITOR	TOTAL ACTIVATION COUNT O NEVER ACTIVATED	
11102	APS DRIVER/MONITOR	TOTAL ACTIVATION COUNT 11	
11103	APS ORIVER/MONITOR	TOTAL ACTIVATION COUNT II	
11301	TVC MONITOR	TOTAL ACTIVATION COUNT 12	
11302	TVC MONITOR	TOTAL ACTIVATION COUNT 21	
11401	MPS TVC DRIVER UNIT	TOTAL ACTIVATION COUNT	
11402	HPS TVC DRIVER UNIT	TOTAL ACTIVATION COUNT	
11403	HPS TVC DRIVER UNIT	TOTAL ACTIVATION COUNT	
10e11	SRB TVC DRIVER UNIT	TOTAL ACTIVATION COUNT	
11502	SRB TYC DRIVER UNIT	TOTAL ACTIVATION COUNT 1 TOTAL ACTIVATION COUNT 2	
11601	ALRO SURFS DRIVER UNIT	TOTAL ACTIVATION COUNT 2 TOTAL ACTIVATION COUNT 2	
11602	AERO SURFS DRIVER UNIT	TOTAL ACTIVATION COUNT 2	
11603	AERO SURFS DRIVER UNIT	TOTAL ACTIVATION COUNT 2	
11604	AERO SURFS ORIVER UNIT	TOTAL ACTIVATION COUNT D NEVER ACTIVATED	
11831 12001	BKUP OPTICAL UNIT PITOT STATIC PRUBE	TOTAL ACTIVATION COUNT 1	
, - 001	t from making a maga	•	

HISSIUN PHASE &

REHUE ZVOUS+DOCK I NU

n						
FHOH 19.20	8 6 HOURS	MET	Ŧυ	25.0000	HOURS	MET

SYSTEM	SYSTEM DESCRIPTION	AVERAGE KN	PERCENT	K P M A X I HUM	TIME OF HAXIMUM
1 2 3 4 6 7 8 21 22 30 31	GUIDANCE, NAVIGATION, AND CONTROL COMMUNICATIONS DISPLAY AND CONTROLS OPERATIONAL FLIGHT INSTRUMENTATION ELECTRICAL POATR DIST AND CUNTROL DATA PROCESSING PAYLUAD HANAGEMENT ORBIT MANEUVERING SYSTEM REACTION CONTROL SYSTEM POMEN GENERATION SYSTEM POMEN HEACT STOR AND DIST ENVIRONMENTAL CONTROL AND LIFE SUP	.89185 .42344 1.50624 .34000 .42013 2.43200 .00685 .74492 .18572 .38022 .04160	9.7453350 4.6364501 16.4927390 3.7228547 4.6002810 26.6293416 0749762 8.7040212 2.0335968 4.1632022 .4555021	.89580 1.00700 1.69338 .34000 .98550 2.43200 .04000 .79492 .37024 .38022 .04160 2.60880	24.43333 24.43333 25.00000 24.43333 24.43333 25.00000 24.43333 25.00000 24.43333 25.00000 25.00000

TUTAL KNH REQUIRED FOR HISSION PHASE 5

TUTAL KNH ACCUMULATED TO HISSION TIME 25.00000 196.15139 KNH

AVERAGE POWER 9.13277 KM
PEAK POWER 10.54945 KW
TIME OF PEAK POWER 23.97833 HOURS

ORIGINAL PAGE IS OF POOR QUALITY

TABLE 2-12. COMPONENT USAGE LISTED BY DECREASING PERCENT OF ENERGY USED

COMPONENT USAGE LISTED BY DECREASING PERCENT OF PODER USED

C	OMPONENT Number	COMPONENT DESCRIPTION	COMPONENT REF POWER LOAD EMATTS; NO	AVERAGE USE FACTOR (PERCENT)	TOTAL ON TIME (HOURS)	COMP ENERGY REQUIRED [MATT HOURS]	PERCENT OF Total Hission Energy required	ACCUMULATED PERCENT
2	501302 /	LUX HOTOR PUHPS (1 ON)	970.00 15	99.9999	89.5455	.848590#D+D		5 + 2175074
2	501301' /	LUK HOTOK PUMPS (1 DN)	970.00 11	#9,999#	87.5455	.84859080+0	5 5.2375076	10.4750152
3	401401	PARIN HEATER	1000+00 1	**,9*99	82.0848	. # 2 O B # 7 2 6 * O	5 4.9497399	15.4247550
4	400101	CABIN FANS CABIN FANS CABIC COMPUTER (48K) CASIC COMPUTER (48K) FREDN PUMPS TOOD MGMT COBBAND XCUR (TORS) XM;¥ 2	319.00 52	149,9998	147.9147	.80348005+0		20.2494519
5	70102	SASIC COMPUTER (48K)	400.00 27	99,9999	147.9147	.47166575+0		24+3 97229
	10101	SASIC COMPUTER (48K)	400.00 25	99.9999	147.9147	+47 64575+0		24.3497939
7	400401 F	REON PUMPS	202.00 52	149.9997	14749167	.50878444D		31+4377217
8	401001 #	TOOD HEHT	1500.00 30	100.0000	30.5448	45#50156+0		34.2024360
7	21304 5	STBAND XCUR (TORS) AHIT 2	220.00 26	99,9999	145.7981	134475523+0		36.4018698
10	10105 1	HU	102+00	99,9999	167,9167	.31044540+0		30,2750273
11	10101 1	· · · · · · · · · · · · · · · · · · ·	185.00 4	99.9999	167,9167	11064540+0		48 - 1 48 1848
12		CP PUHPS	180.00 18	99.9999	167:9167	10224941+0		41+9707:45
13		CP PUMPS ITHS 02 ITHS 02 ITHS 02 ITHS 02 ITHS 02 ITHS 04 IT	180.00 10	99.9999	167.9167	+3D224941 *D	•	43.7132482
1.7	311702 F	1785 02	500:00 2	38+0000	167,9167	.25187463+D		48+3150540
15	311701 +	1TRS 02	500.00 L	30+0000	147.9167	25 87443 ° D		44.8308010
1 6	401003 F	DOD HENT	100+00 52	149.9998	167+1080	+25044154*D		48+3422627
17	70202 0	into I/o units	125.00 27	99.999	16719167	+20789554+0		47+6079097
1.8		SN°C 1/0 UNITS	125.00 25	99.999	167:9147	*20*8*554*0		50.0735544
17		ISPLAY PROCESSORS	120.00 27	99,9999	167.9167	+2014997B+0		\$2+0485782
20		ISPLAY PROCESSORS	120+00 25	99.999	167.9167	-20149978+01		53+3035998
21		VIONICS BAY FANS AFT	80.00 53	149.9998	167.9167	+20147970+0		54+5 84210
22		IVIONICS BAY FANS AFT	BD - 00 52	149.9948	167,9167	42014976+01		55.7334421
23			64.70 54	149.7998	147:9147	+142942B7+G		54+7162900 57+4989379
24		AVIONICS BAY FANS FUO	44.70 53	149.9996	147.9147	+14294287+0		
25		AVIONICS BAY FANS FUD	64+70 52	14949998	147,9147	* 142942#7*0! *14484449*0!		50:411505B 51:5547751
26		ASTE POT WATER	460.00 28	79:5316 79:5316	3915923 3915923	.14484669+0		40+4284043
27		MASTE*POT WATER	460.00 26	149.9998	16719167	13349358 ° D	. = '	41 + 2133540
28		ATER PUMPS	53.00 54 75.00 3	99.9999	167.9167	12573732+0		41.7127440
29		OAD CONTR ASSYMAFT	75+00 2	99,9799	147.7167	12593732+0		62+7521319
30		OAD CONTR ASSYMAFT	75.00 1	99.9999	16719167	•12593732+p		63.5115199
35 31		.OAD CUNTR ASSY#AFT .OAD CONTR ASSY⇒FWD	75.00 30	99.9947	47.9147	12573732+0	•	64.2707074
33		OAD CONTR ASSYTEMD	75.00 24	99.9999	147.9147	12593732+0		45.0302754
34		DAD CONTR ASSYFFAD	75.00 26	99.999	167.9167	12593732+0		45 - 7894833
35		CH HASTER UNIT	40.00 24	99.9999	47.9147	10074989+0		46+3971939
34		CRT DISPLAY UNITS	60.00 7	79,7799	16719167	10074789+0		47 - 0047045
37		CRT DISPLAY UNITS	60.00 6	99.9999	147.9147	10074989+0		67-4122150
3 / 3 8		LET AVIONICS HIR	400.00 1	100+0000	23.2011	92804200+0		48 - 1718140
39		THERMAL CONTROL HTRS	400.00 3	000000	23,0707	123621021D		68.7287621
40		HERHAL CONTROL HIRS	400.00 2	100.0000	23,0907	17234270270		49 - 2054903
41		INSTRUMENTATION	50.00 1	99.9999	14719147	41958219+0		67.7117463
42		UX HEHORY (14K)	50.00 27	99,9999	16719167	.83958219+D		70 - 2 + 8 20 4 3
43		AUX HEHORY (14K)	50.00 25	99,9999	147,9147	#83750219+D		78.8044443
44		IAIN ENGINE HEATERS	300.00 3	100+0000	23:0707	. 64272177+Q		71 - 2221440
45		IAIN ENGINE HEATERS	300.00 2	0000.001	23.0907	+49272177+0		71+4370714
		·n··· - · - · · · · · · · · · · · · · ·	- - -		•			

TABLE 2-13. SUBSYSTEM USAGE SUMMARY

COMPONENT USAGE LISTED BY SUBSYSTEM

LIVINGHENTAL CONTROL AND LIFE SUP

C	uni anger tideografi	COMPONENT	DESCRIPTION				AVEHAGE USE FACTOR (PEHCENT)	TOTAL ON TIME (HOUN)	HEGS	ENERGY Hours	TOTAL	CENT OF HISSION HEWUIRLD	
1	400101 64	HIN FANS			439.65	5.2	99.9999	167.9167	.723	113217+0	5 19	.4544090	
ž	HEDIO! CA				440.65	51	10200	• 0000	• 000	100000		•0000000	
3	469ini Ca				430.65	54	•3000	+8886		100000		•0000000	
4		31N PRES5+42	STOP		14.31	52	99.9998	167.9167	+ 240	328834+0) 4	.6531064	
5		BIN PHLSS+HZ		•	14.31	5.3	.0000	•0000	.000	100000	,	•0000000	
6		815 PRESS+82			14.31	54	• 5000	•0006	• 000	200000		•0007000	
- 1		TER PUMPS			71.55	54	99.9999	167 • 9167	• 120)14420+() S)	• 2655320	
8		ILA PUHPS			71.55	52	• 0000	•0000	• 000	00000		•0000000	
9		ILR PUMPS			71.55	53	•0000	•0000	+001	300000		•0000000	
10	-	LGN PUMPS			272 - 70	52	99.9999	167.4167	• 45	/40809+i)5 12	• 4459904	
11		LEON PUNPS			272+70	53	• 0 0 0 0	•0000	• 000	000000		•0000000	
12		CUM PUPPS			272.70	54	-0000	• 0000	• BO1	000000		•0000000	
13		NOTANIJBUC U	SISTEM		27 • 60	52	99.9999	165 - 1069	444	578810+0	34 l	.2116566	
14		U SUBLINATUR			27.00	53	•0000	•0000	• 003	000000		•0000000	
15		O SUBLIBATOR		`	27.00	54	• 0000	•0880	• 000	აიიიია		•0000000	
16		O SUBLIMATOR		•	23.30		•0000	•0000	• 000	000000		.0000000	
17		O SUBLIMATOR			20.00	2	.0000	•0000	.000	00000		.0000000	
18		U SUBLIMATOR			20.00	3	•0000	•0000	•00	000000		•00000000	
19		ACE RAD CONTI			20.00	1	99.9999	165-1069	+33(321349+6	34	.8975238	
20		ACE HAD CONTI			20.00	2	•0000	•0000	• 801	00000		•0000000	
21		ACE RAD CONTR			20.40		•0000	•0000	•000	000000		•0000000	
42		HUNIA CONTR (25+30		50.0000	1 4 8 2 6 4	22	329866+6	} 2	.0062052	
23		HUNTA CONTR I			25.00	-2	• ព្យព្ធ	•8000	_	000000		•0000000	
24	400701 67				184.00		67.2507	32.8675		779601+(•1355740	
25	400962 4/				189.60	53	67.2567	32.8475		779601+0		•1355740	
26	400963 %	STE MGMT			189.00	54	67.2567	32.8675		779601+1	- :	•1355740	
27	4604[4 10]	STE HONT			10.00	l i	67.2568	32 . 8675		105412+0		•0000833	
28	400905 11				19.30	2	67.2548	32.8675		105612+1	-	.0600833	
29	400966 W				10.00	3	47.2548	32.8675		105612+1		.0600833	
30	401001 F	OU HGHT			1500+60	3.3	190,0000	30.5668		850156+	05 12	• 4621209	
31	401002 FO				1500.00	26	*000 0	•0000		000000		•0000000	
32	401001 F	OD MGHT			172.00		100.0000	30•5468		265141+1	04 1	+1215909	
33	401004 F	OO HGMT			135.00	53	•0000	•0000		000000	_	•0000000	
54	401101 %	ASTE+PUT HATE	R		16+20	-	79.5316	39.5923	_	011224+		-1366490	
35	401162 1.	ISTE+POT HATE	k		16.20		79.5316	39.5923	_	011224+		+1386498	
36		ASTE+PUT HATE			460.00	26	79.5316	39.5923		484669+1	•	. 9369485	
37		ASTE POT HATE			400.00		79.5314	19.5923		484669+		1 9 3 6 9 4 8 5	
3.8		LIONICS BAY F			87.34		99.9999	167 - 9167		666664 *1	-	9864149	
39	401262 A	VIUNICS BAT F	ANS FWD		87+34		99,9999	167.9167	•	666664+1		. 9864149	
40	4012G3 A	VIONICS BAY F	ANS FHD		87.34		99.9999	167 9 167	-	666664+1) b 3	.9864149	
41	4013n1 A	VIUNICS BAY F	ANS AFT	~	100.00	52	.0000	•0000		000000		.0000000	
42		VIONICS DAY F			108 • 00	53	• 0 0 0 0	•0000		000000	_	•0000000	
43		ABIN HEATER			1000+00		99.9999	82.0530		052901+		.3020654	
44		NSTRUMENTATIO	h .		50.00	1	99.9999	167.9147	.83	958220+1	א 2	5 • 58 1 4 4 7 3	

TABLE 2-14. SUBSYSTEM SUMMARY BY MISSION PHASE

SUBSYSTEM USAGE SUMMARY BY MISSION PHASE MISSION PHASE I THRU ID

SYSTEH	SYSTEM DESCIPTION] K#H	2 K#H	7 7	4 K#H	S Krh	6 K#H	7 Kwh	8 KWH	9 €# H	lo Kah
1	GUIDANCE, NAVIGATION, AND CONTROL	751.55	484.43	500.07	434.47	474775	535+ĝ#	599+17	473+20	473+20	
ž	CONHUNICATIONS	643.54	459.60	459.60	459,40	459,40	496.72	1331144	923,33		
3	DISPLAY AND CONTROLS	1224 - 05	648.56	707+21	903.69	444.23		1147.73			719.26
Ÿ.	OPERATIONAL FLIGHT INSTRUMENTATION	573.58	523+12	523.12	523.12	523+12	523+12	523+12	523+12	523+12	523 - 12
5	DEVELOPMENTAL FLIGHT INSTRUMENTATION	•00	•00	.00	•00	.00	•00		• 00	• 00	• 00
6	ELECTRICAL POWER DIST AND CONTROL	634.59	750.00	750.00	750.00	750+00	783.21	1230:00	1104.44	750+00	750.00
7	DATA PROCESSING	2565 . 06	1460.00	1519+63	1822.84	1466+33	1597.29	1739+54	1460.00	1440+00	1544.55
a	PAYLOAD HANAGEMENT	•00	.00		•00		• Ó O	• 0 0	• 00	+00	* D G
9	UNHANNED KIT	+00	•00	• 00	+00	• 80	• 0.0	•00	• 0 0	•00	* D O
15	EXTERNAL TANK (ET)	34,24		70+77	+00	• 00	• 80	• 00	• 00		• 0 0
1 6	SULID ROCKET BOOSTER (SRB)	•00	.00	• 00	.00	• 00	• 00	+00	• 0 5	• 00	• 00
20	HAIN PROPULSION SYSTEM	332.41	• 0.0	• 60	•00	,00	• 0 0	+50	:00	• 00	• 00
2 i	ORBIT HANEUVERING SYSTEM	52.52	204.64	89:20	542.74	12,44	205+34	418+17	• 00		134.50
22	REACTION CONTROL SYSTEM	53+51	493.04	332.07	125.04	269:40	364.08	313+93	352 • 42		341.52
23	AIR BREATHING ENGINE SYSTEM	.00	•00	• 00	•00	• 00	• D O		.00		•00
30	POWER GENERATION SYSTEM	. 2101+62	380+22	180.22	380 - 22	380.22	380.22	3#0+22	3 4 0 + 2 2	340+22	350.22
31	PUMER REACT STOR AND DIST	351 + 88	417.00	417+00	417.00	417.0B	417.00	417+00	417+00	417.00	417.00
32	AUX POWER UNIT	402+08	400+00	337+56	+00	• 00	• 00	• 0 0	• 00		• 0 0
40	ENVIRONMENTAL CONTROL AND LIFE SUP	1489.57	4008+05	4102.28	4207+28	2447122	2184+29	2141157	2124+15	2414+17	2452 • 74
5 a	HYDRAULICS POWER SYSTEM	27:67		• 00	•00		+00	• 80	• 0:0	1072+04	1382:46
51	DOCKING AND CARGO HNDLG	• 0 0	•00	•00	• B G	• 00	• 00	• 6 6	•00	•00	• 0 0
52	HECHANICAL SYSTEM AND LDG	119.53	2540.00	441.95	•00	• 00	• 00	100	• 0.0	• 00	+00

TABLE 2-15. SEPS PHASE 1 PLOT TAPE

WORD LOCATION	PURPOSE
1	Time
2	Total dc load
3	Total fuel cell load includes losses
4	Fuel cell load
5	Fuel cell 2 load
6	Fuel cell 3 load
7-106	Subsystem load

2.6 PHASE I INTERFACE TAPE

Phase I of the SEPS Program provides as an output an interface tape which contains event time point data including time, loads, power factors, and circuit switch positions to drive Phase II through a defined mission timeline. Associated with this interface tape, a compacted component dictionary built in Phase I is required and it can be stored on the interface tape or on another tape The storage location of the compacted dictionary and the interface tape is controlled by user and is defined to the program through the "units" card. Explanation of card usage is in Section 4.

3.0 SEPS PHASE II

3.1 DISCUSSION

The SEPS Phase II program integrates a collection of math models that define the operating characteristics of the power sources, distribution, and equipment of the input electrical power system. This provides the capability of simulating the total electrical power system with which system design and design/mission requirements compatiblity can be analyzed and parametric studies performed by making variable data and timeline changes. The program, using an input circuit definition of the electrical power system, solves the electrical circuit at each defined time point to a defined load configuration. The derived information is then provided as output data.

The Phase II outputs available to the user are:

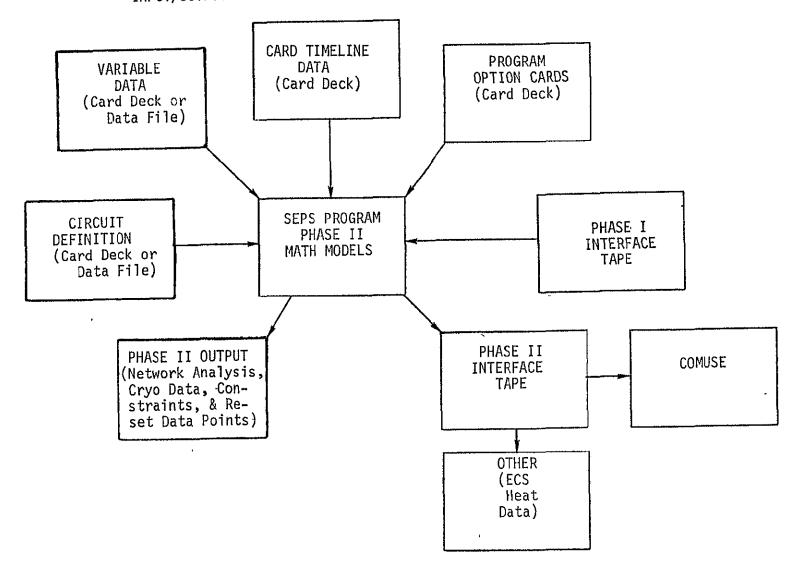
- FORMATTED PRINTOUT dc DISTRIBUTION NETWORK, SOURCE, AND INVERTER STATUS printout occurs whenever Phase II is run.
- 2. CRYOGEN USAGE printout occurs whenever Phase II is run.
- INTERFACE TAPE Similar to Phase I interface tape and is available at user option.
- 4. COMUSE Requires interface tape and is available at user option.
- 5. CONSTRAINT VIOLATIONS User Option

The inputs for Phase II operation can be grouped in the following categories:

- 1. CIRCUIT DEFINITION DATA
- 2. VARIABLE DATA
- 3. CARD TIMELINE DATA
- PROGRAM OPTION CARD DATA
- 5. INTERFACE TAPE DATA

Each input is described in detail in Section 3.2. See Figure 3-1 for Input/Output Flow Diagram.

FIGURE 3-1
INPUT/OUTPUT FLOW DIAGRAM OF SEPS PHASE II



Phase II can be operated in three different modes dependent upon what type of timeline is used. The three modes are:

- 1. Card deck timeline mode Would be used to perform simple parametric studies of the electrical power system. A sample listing of input deck is shown in Appendix D.
- Interface tape timeline mode Would be used to perform mission analysis. A sample listing of input deck is shown in Appendix E.
- 3. Interface tape with timeline adjustments mode Would be used to perform system contingency operation analysis and equipment design/ mission requirements capability. A sample listing of input deck is shown in Appendix F.

3.2 PHASE II INPUT DATA

3.2.1 Circuit Definition

A description of the dc distribution circuit and a definition of an associated inverter system is provided as input to Phase II. The dc distribution circuit is defined in terms of branches and the elements contained in each branch (such as sources, loads, diodes, etc.). The inverter system is defined in relation to the dc circuit and in terms of the number of inverters, the number of associated dc loads, the number of ac buses, which inverter is connected to which ac bus and where the input inverter dc load is to be applied within the dc circuit.

3.2.1.1 Circuit Elements

The elements that are used to make up the circuit description are categorized by sources and loads. A maximum of twelve sources in a circuit description can be handled by Phase II. The type sources that can be used in the circuit description are:

- 1. Fuel Cell maximum of 5
- Battery maximum of 6 (2 types of I-V curves allowed)
- 3. Other maximum of 12, these are simulated by an input I-V curve for each source (such as a transformer rectifier). Maximum of 2 input I-V curves for "other" sources (Type 3 or Type 4).

The types of load components that can be used in simulating the electrical circuit are:

- dc equipment loads includes constant power, constant resistance and three point load data.
- ac inverter and ac equipment loads in the circuit analysis, these loads are converted to the amount of dc power required for ac operation including efficiency losses (cannot be combined with other loads in the same branch).
- 3. Remote Power Controller (RPC) includes a voltage drop, forward resistance, reverse resistance and no-load resistance.
- 4. Diode includes a voltage drop, forward resistance and reverse resistance.
- 5. Battery charger Load required to recharge battery, value changes to zero charge load when SOC reaches 100%. (cannot be combined with other loads in the same branch)
- 6. Line loss line resistance between two nodes.

A total of fifty DC LOADS can be used in developing the input circuit description. A LOAD can be any combination of the components listed above with the exception of the two (2 and 5) so stated above.

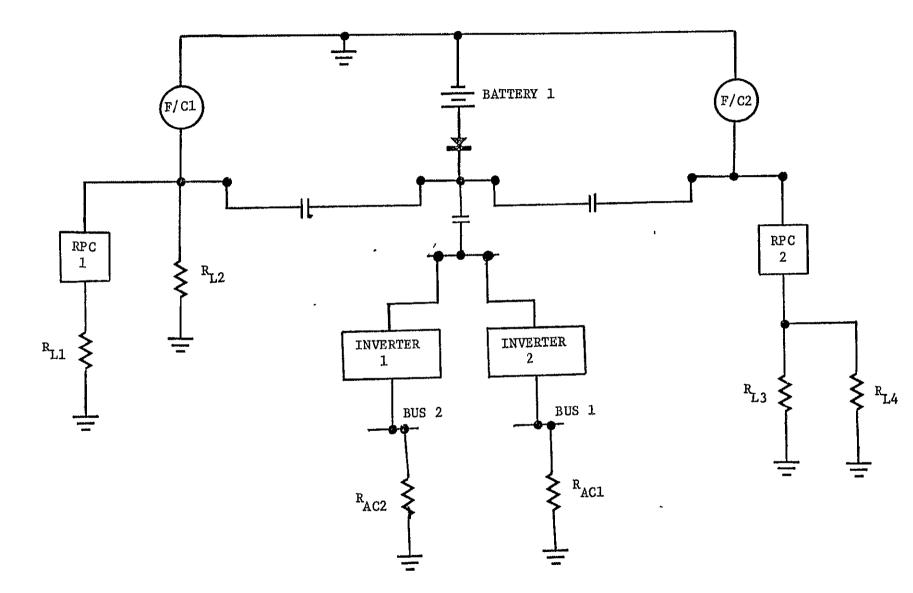
3.2.1.2 Distribution Circuit Description

The distribution circuit is defined in terms of branches and nodes. This information is provided as an input to the computer program. For Phase II, a branch is defined as an electrical circuit path that contains an element (source, loads, line resistance, etc.,) and a switch. A node is a junction point of two or more branches.

3.2.1.3 Simple Circuit

Figure 3-2 is a typical circuit that contains most of the elements available for program circuit analysis. Some general rules that must be followed in describing the circuit in branches and nodes are:

- 1. Loads used for the charger and those used for inverter dc loads cannot be used for any other loads.
- 2. Branch switches and lead resistances will have the same number as the branch in which they are located.



35

- 3. Each branch will contain a switch and a lead resistance and the value of the resistance must be greater than .0005 ohms except when the branch contains a load.
- 4. A load cannot be included in a branch that contains a source.
- 5. All real (R) data values must have a decimal point.
- 6. Maximum of 30 nodes (including ground or reference node)
- 7. Maximum of 9 inverters
- 8. Maximum of 100 branches
- 9. Maximum of 12 sources
- 10. Maximum of 50 loads
- 11. Maximum of 100 diodes including RPC's
- 12. The Branches which contain a source, the current flow must be defined from the lower voltage to the higher voltage node.
- 13. Branches which contain a diode or a RPC, the current flow definition must be in the positive direction of diode or RPC flow.
- 14. Data on each card should start in column 2.
- 15. Sources are to be numbered consecutively with a maximum number of 12.

Figure 3-3 shows the typical circuit with the circuit defined in terms of branches, nodes and elements required for coding into the SEPS program.

3.2.1.4 Circuit Input Data

The data describing the circuit is read from a card image tape file or from a card deck input to the data tape through program WLCCIT in forming a new file on the data tape. To develop the tape file, the format used to input the circuit description is a Free Field Format, i.e., each value is to be separated by a comma, all data must be listed in proper sequence on each card, and all data must be present even if the value is zero or blank. The end of each card is used as a comma. Figure 3-4 shows the required card deck set up and Figure 3-5 states what data information is to be included on each card. Figure 3-6 is a listing of the card deck used to describe the simple circuit in Figure 3-3.

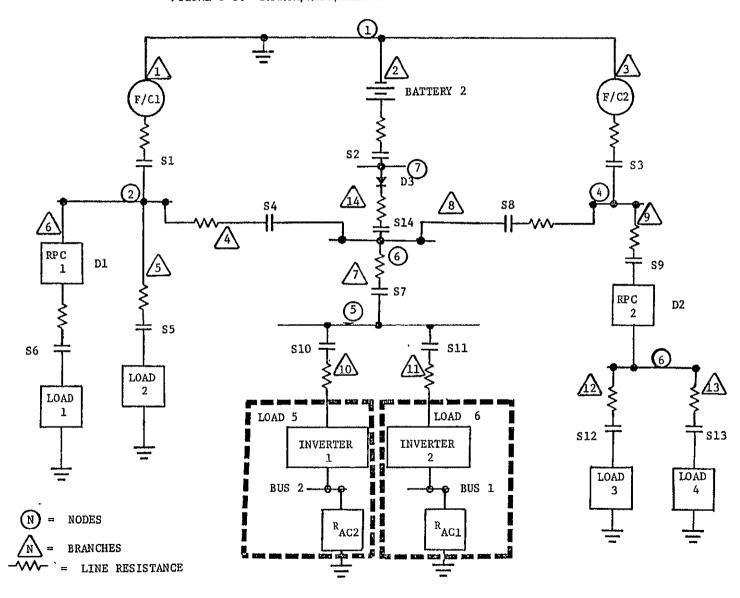


FIGURE 3-3. BRANCH/NODE/ELEMENT CONFIGURATION OF SIMPLE CIRCUIT

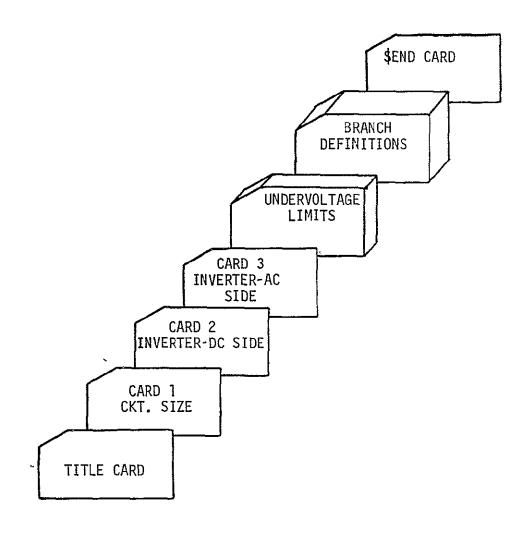


FIGURE 3-4. - CIRCUIT DEFINITION DECK SETUP

FIGURE 3-5.
CARD DATA (FREE FIELD FORMAT)*

CARD	VARIABLE TYPE		CARD INFORMATION
TITLE CARD			NAME GIVEN TO CIRCUIT
CARD 1	I	1.	No. of Nodes (including ground or reference node) (maximum of 30)
	I	2.	No. of Reference Nodé
	I	3.	No. of Inverters (maximum of 9)
	I	4.	No. of the Load reserved for the battery charger. If battery charger not used, insert zero as load number.
CARD 2	I	1.	Quantity of D-C Loads used for Inverter Loads (minimum of 1)
	I	2.	Branch Nos. containing the d-c Loads (each branch number seperated by a comma)
	I	3.	Inverter numbers associated with the above dc LOAD (listed in corresponding sequence as branches)
CARD 3	I	1.	Quantity of A-C Load Buses supplied by inverters (minimum of 1)
	I	2.	Inverter No. (relative no.) connected to AC Bus 1
	I	3.	Inverter No. (relative no.) connected to AC Bus 2
	I	4.	Inverter No. (relative no.) connected to AC Bus 3
UNDERVOLTAGE LIMITS	R.	1.	One under voltage limit for each node (number of nodes taken from card 1). If no under voltage limit applies, use -10. for the limit. Separate each value by a comma. Use as many cards as necessary.
BRANCH DEFINITIONS	I	1.	Branch number (maximum and largest allowable is 100)
(one card per branch, each	I	2.	Node number from which current flows
card containing	I	3.	Node number into which current flows
the information described at the	I	4.	Source number (maximum and largest allowable is 12)
right.)	I	5.	Source type (Type 1 = Fuel cell, Type 2 = Battery, Type 3 = OTHER 1, Type 4 = OTHER 2)
	I	6.	Relative source number (relative to type, such as fuel cell 1, 2 and 3) (maximum of 5 fuel cells and up to 6 batteries)

FIGURE 3-5. (CONTINUED)
CARD DATA (FREE FIELD FORMAT)*

CARD	VARIABLE TYPE		CARD INFORMATION
BRANCH DEFINITIONS (CONTINUED)	I	7. 8.	Load number (maximum and largest allowable is 50) Relative diode number (maximum and largest allowable
•			100)
	R	9.	Lead resistance of branch (minimum value of .0005)
	R	10.	Diode or RPC voltage drop
	R	11.	Diode or RPC forward resistance
	R	12.	Diode or RPC back resistance
	R	13.	RPC no load resistance value at 28 volts which is to be used to calculate RPC no load power loss
	R	14.	Branch current limit
	I	15.	Switch position of branch switch (initial condition) (closed = 1, open = 0)
END CARD		1.	This card is used to end the circuit definition. Place a - 10 followed by 14 commas on the card.

^{*}Start each card in column 2

DRIGINAL PAGE IS		FIGURE 3-6	INPUT CARÐ LISTING - EXAMPL	E -	
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	<u> </u>	_ 	<u> </u>		
CARD 2	213111013111111111111111111111111111111		<u> </u>	1111111111111	<u>,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,</u>
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3.2.2 Variable Data

Input variables for Phase II are provided to extend the flexibility of the program for conducting electrical power system parametric studies and to eliminate many program modifications that would occur due to the evolution of vehicle and hardware design. Table 3-1 lists all Phase II variables with maximum dimension, input method, units, and definition.

The two methods of inputing the variable data into Phase II are by data tape and card deck. The method used by each variable is defined in Table 3-1. The values of the variables that are listed on the data tape will be overriden by card deck input. The only card deck modifications that can be made to the Phase I interface tape are the changing of a component power level at data initialization and changing a switch position during the timeline.

Figure 3-7 shows the card deck setup for inputing the initialization data. Figure 3-8 shows the individual card readout for the initialization input data for the circuit described in Figure 3-3. When the Phase I interface tape is used, the card deck input LOAD VARIABLES in Table 3-1 are not required since load values would be obtained from the interface tape timeline.

Changing the variable values through the card deck timeline input is discussed in Section 3.3.

3.3 CARD TIMELINE DATA INPUT

The card timeline provides a means of access to the Phase II program during a timeline run to inject changes at desired time point.

The type of change inputs available to the user are:

- 1. Load values
- 2. Switch position (changes circuit configuration)
- 3. State-of-charge on battery
- 4. Operating temperature of fuel cell
- 5. Temperature of battery

TABLE 3-1. INPUT DATA VARIABLES FOR PHASE II

			THEOL	ME LUOD		<u> </u>
	LABEL	MAX DIM	DATA TAPE	CARD* DECK	UNITS	DEFINITIONS
	ACPOW(I)	(9)	Х	ID&CTL	WATTS	LOAD VARIABLES Ac load for inverter AC BUS (I), Variable not used when Phase II is driven by an
	PFAC(I)	(9)	х	ID&CTL '		interface tape AC load power factor for AC BUS (I) corresponding to loads in ACPOW(I). Not used
46	PP(I)	(50)	Х	ID&CTL	WÄTTS	with interface tape Constant power load for LOAD (I), Variable not used when PHASE II is driven by an interface tape
	PR(I)	(50)	, x	ID&CTL	WATTS	Constant resistive power load for LOAD (I), PP(I) and/or PR(I) may be used to represent LOAD (I), Variable not used when PHASE II is driven by an interface load
	CHRGLD]	Х	ID&CTL	WATTS	DC power required by battery charger when charging batteries

*ID = INITIALIZATION DATA

CTL = CARD TIMELINE

	,	INPOT ME	THOU		
LABEL	MAX DIM	DATA TAPE	CARD* DECK	UNITS	DEFINITIONS
CVAL(I)	(25)	Х	IĐ		LOAD VARIABLES Component power value to be used in PHASE II calculations in lieu of the component loads in TPLOAD. NOTE: LOC 1-5 for F/C 1-5 noncyclic loads and LOC 6-10 for F/C 1-5 cyclic loads
NCNT(I)	(25)	. х	ID	er edekarativa errorrativa	Component numbers associated with the component loads in CVAL
NCTP(I)	(25)	Х	ID	Laboration of the Control of the Con	For each LOAD in CVAL, defines if the load is constant power (>0) or constant resistance (=0)
NCNTC		Х	ID		Number of loads defined in CVAL
CAPINV	(9)	Х	ID	VOLT- AMP	Inverter (I) maximum overload limit

*ID = INITIALIZATION DATA

CTL = CARD TIMELINE

TABLE 3-1. INPUT DATA VARIABLES FOR PHASE II (Cont'd)

	·	INPUT	METHOD		
LABEL	MAX DIM	DATA TAPE	CARD* DECK	UNITS	DEFINITIONS
FCTA(I,J)	(9,7)	X	ID		FUEL CELL VARIABLES Array containing fuel cell I-V curves as a function of temperature I = No. of points J = 1 is current value (amps) J = 2 thru 7 voltage level at each temperature of FCTN
FCTN(I)	(6)	Х	ID		Temperatures associated with the I-V curves of FCTA
FCT(I)	(5)	Х	ID&CTL		Operating temperature of fuel cell (I)
NFCTA		Х	ID		No. of current points used in FCTA
H2T		Х	ID	LBS	Initial quantity of hydrogen available
02T		Х	ID	LBS	Initial quantity of oxygen available
HUR		Х	ID	LBS/ AMP-HR	Hydrogen use rate based on amp-hour requirements
OUR	<u> </u>	Х	ID	LBS/ AMP-HR	Oxygen use rate based on amp-hour requirements
HR		Х	ID	LBS/ HR	Hydrogen purge rate
OR		Х	ID	LBS/ HR	Oxygen purge rate ,

^{*}ID = INITIALIZATION DATA CTL = CARD TIMELINE

	 	TMLOI	IJE I UOD		
LABEL	MAX DIM	DATA TAPE	CARD* DECK	UNITS	DEFINITIONS
					FUEL CELL VARIABLES
НРТ	ļ	l x	ID	SEC	Hydrogen purge time
OPT		X	ID	SEC	Oxygen purge time
PIH		Х	ID	HRS	Interval between hydrogen purges
PIO		х.	ID	HRS	Interval between oxygen purges
TPH(I)	(5)	Х	İD	HRS MET	Time of last hydrogen purge for fuel cell (I)
TPO(I)	(5)	Х	ID	HRS MET	Time of last oxygen purge for fuel cell (I)
WPR		Х	ID	LBS/ AMP-HR	Fuel cell water production rate
H2U		X	ID	LBS	Unusable hydrogen quantity
02U		Х	ID	LBS	Unusable oxygen quantity
FCLIM(I)	(3)	Х	ID	WATTS	Fuel cell power limits, l = peak, 2 = average, 3 = minimum
TIMV(I)	(3)	X	ID	HRS	Length of time that fuel cell power limits apply, l = peak, 2 = average, 3 = minimum
TMAXFC		х	ID	HRS	Maximum time step thru fuel cell thermal model
	1	1	1		1

^{*}ID = INITIALIZATION DATA CTL = CARD TIMELINE

49

TABLE 3-1. INPUT DATA VARIABLES FOR PHASE II (Cont'd)

INPUT METHOD LABEL MAX DATA CARD* UNITS MIG TAPE DECK DEFINITIONS FUEL CELL VARIABLES FCWP1(I) (5) Parasitic pump and logic loads for fuel cell Χ WATTS ID (I) - constant power FCWP2(I) (5) Χ ID WATTS Parasitic heater cyclic load for fuel cell (I) constant resistance SSTVI(I,J) (10.2)Χ ID °F/ The T-I curve which the fuel cell follows as the fuel cell temperature reaches its steady state AMP value. J = 1 is Temp., J = 2 is current Х ٥F Fuel cell lower temperature limit - heaters FCLTL ID turn "ON". **FCHOL** ٥F Χ ID Fuel cell high temperature limit - heater turns "OFF". **FCHTL** ٥F Fuel cell redline limit - diagnostic warning X ID BATTERY VARIABLES CSUBD(I) (6) Battery amp-hour capacity for battery (I) Χ AMP-ID HRS SOC(I) (6) PER-Battery (I) initial state-of-charge Χ ID& CTL CENT

^{*}ID = INITIALIZATION DATA

CTL = CARD TIMELINE

		#141 C	i PILTIOD	· · · · · · · · · · · · · · · · · · ·	
LABEL	MAX DIM	DATA TAPE	CARD* DECK	UNITS	DEFINITIONS
					BATTERY VARIABLES
TB(I)	(6)	Х	ID& CTL	°F	Battery (I) temperature
XNC(I)	(6)	Х	ID		Number of cells in battery I
EFFAVR		X	ID,	PER- CENT	Average amp-hour efficiency during charging
SOCA(I,J,K)	(7,6,2)	X	ID		Battery I-V curves versus temperature, and battery group. I = No. of points, J = 1 is current, J = 2 to 6 voltage at temperature of IT, K = battery group 1 or 2 (1 = group of 3 common batteries) (2 = group of common batteries). (per cell voltage)
IT(I,J)	(5,2)	X	ID		Battery temperatures used in SOCA, I = temperature, J = battery group
NSOCA		x x	ID		No. of points used in SOCA for each IV curve
SOCUL		Х	ID		Minimum battery SOC limit

^{*}ID = INITIALIZATION DATA

CTL = CARD TIMELINE

TABLE 3-1. INPUT DATA VARIABLES FOR PHASE II (Cont'd)

		IMPUL	עטחו אויי	···	
LABEL	MAX DIM	DATA TAPE	CARD* DECK	UNITS	DEFINITIONS
R(I) S(I)	(100) (100)	X X	ID ID& CTL	OHMS	DISTRIBUTION CIRCUIT VARIABLES Branch (I) line resistance Branch (I) switch position (1 = closed, 0 = Open)
ICRCT(I,J)	(100,4)	X	ID		Branch (I) definition I = Branch No., J = 1 is node number current out, J = 2 is node number current in, J = 3 is source number, J = 4 is load number
NCRT		. х	ID		Maximum or highest branch number in ICRCT
CRCT(I,J)	(100,6)	X	ID		Branch (I) diode or RPC definition I = Branch No., J = 1 is diode voltage drop, J = 2 is diode forward resistance, J = 3 is reverse resistance, J = 4 is RPC no load resistance, J = 5 is forward or reverse resistance being used, J = 6 is branch current limit
UV(I)	(30)	X	ID	VOLTS	Undervoltage limit for node (I)
DELTA		X	ID& CTL	VOLTS	Tolerance on node voltage solutions (normally 10^{-5})

^{*}ID = INITIALIZATION DATA

CTL = CARD TIMELINE

		1111 01	טטחומיי			
LABEL	MAX DIM	DATA TAPE	CARD* DECK	-	UNITS	DEFINITIONS
INVLCA(I)	(9)	X X	ID& CTL			DISTRIBUTION CIRCUIT VARIABLES Invérter number connected to AC inverter bus (I)
NSC(I)	(12)	x	ID			Number of IV points in Source (I) used in the circuit solution
T3SRCS(I,J)	(15,2)	Х.	ID& CTL			POWER SOURCE VARIABLE (Other than Batt & F/C) Type 3 source IV curve, I = Number of points, J = 2 is current value, J = 1 is voltage value
NT3S		Х	ID& CTL			Number of points used in T3SRCS
T4SRCS(I,J)	(15,2)	Х	ID& CTL			Type 4 source I-V curve, I = Number of points, J = 2 is current values, J = 1 is voltage values
NT4S		Х	ID& CTL			Number of points used in T4SRCS

^{*}ID = INITIALIZATION DATA CTL = CARD TIMELINE

	·		,		,	
LABEL	MAX DIM	DATA TAPE	CARD* DECK		UNITS	DEFINITIONS
,						MISC. VARIABLES
ET			ID		HRS	Simulation start time
DELTA			ID& CTL		HRS	Maxımum simulator time increment
PRT			ID& CTL			Formatted printout interval as a multiple of TDELTA
PRINT			ID		,	Flag to request initialization data to be printed out. Value > 1
PRNT			CTL			Flag to request a formatted printout at a particular card timeline point. Set = 1 to request print
SOLVC			CTL			Flag to request a circuit solution at a particular card timeline point. Set = 1 to request solution
PRNT1		·	ID& CTL			Set > 0 to request tape timeline debug print
PRNT2			ID& CTL			Set > 0 to request inverter debug print
F	PRINT PRNT SOLVC PRNT1	PRINT PRNT SOLVC PRNT1	PRINT PRNT SOLVC PRNT1	PRINT ID PRNT CTL SOLVC CTL PRNT1 ID& CTL ID& CTL ID ID ID ID ID ID ID ID ID ID ID ID ID I	PRINT CTL PRNT CTL SOLVC CTL PRNT1 ID& CTL ID& CTL ID& ID& CTL ID& ID& ID& ID& ID& ID& ID& ID	PRINT ID PRNT CTL SOLVC CTL PRNT1 ID& CTL PRNT2 ID& CTL IDA IDA CTL IDA

^{*}ID = INITIALIZATION DATA CTL = CARD TIMELINE

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TABLE 3-1. INPUT DATA VARIABLES FOR PHASE II (Cont'd)

INPUT METHOD

ID&

CTL

CTL

LABEL MAX DATA CARD* UNITS DIM TAPE DECK **DEFINITIONS** MISC. VARIABLES MPRNT3 ID& Not Used CTL MPRNT4 ID& Set > 0 to request battery and fuel cell CTL debug print MPRNT5 ID& Not Used CTL

ICHRG(I) (3) CTL Charge Flag to initiate charging of battery (I)

A request to print SIMPAC data for simulation initialization data points.

Not Used

Set > 0 to abort simulation

^{*}ID = INITIALIZATION DATA

CTL = CARD TIMELINE

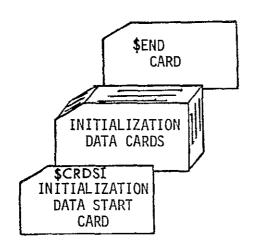


FIGURE 3-7. INITIALIZATION DECK SETUP

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150 O	NO OF CAROS		CODING FORM	D7094 SYMBOLIC	VERIFIED EY		
	SYMBOL OPERATION CHOUNGER IN FORTRAN STATEMENT	ADDRESS TAG DECREMENT				SEQUENCE	
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START CARD-	SICIRIDIS TI	, <u>,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,</u>	<u>,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,</u>	<u>,, , , , , , , , , , , , , , , , , , ,</u>			
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	START CARD FOR INPUT OF	INITILIZATION DATA		i			
		<u> </u>		<u> </u>	<u>-lllllllll</u>		
VARIABLE CARD	- AICIPIOM (1) 1=11471	عالما الماكارة الماكارة الإنهام الم	. 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 	<u> </u>	1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.		
		<u> </u>	i 	; <u> </u>		11111	
		THE VARIABLE VALUE FOR	AC POHER (2)				
		VARIABLE VALUE FOR AC POWER					
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	MHICH VARIABL	,t;; 	<u>,</u>				
	VARIABLE NAME	 					
		1					
	iDIEILITIA jai 1111E1-131	<u>- - - - - - - - - - - - - - - - - - - </u>	<u> </u>	<u> </u>	<u> </u>		
	FICITI 17 = 11.81014141	<u> </u>		<u> </u>			
	H121T1 1= 1211E14131 1.1	<u> </u>		<u> </u>	<u></u>		
		1			,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		
	1021T1 = 1.181E131 1 1		<u> </u>	 			
	PIFIAICI (T) 1 = LIAIBIA	<u> </u>			أروأ والمراجة والواصادات	أحماسيا مراجعا والمجاهدين	
	P181 11 = 5/2/11/1/3/6/	2 1 1 1 6 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1			<u> </u>	1.1.1.1.1.1	
	! !!						
	P.R. 141 = 141516141	<u> </u>	<u> </u>				
	P1P1 13 = 131819141 1		<u> </u>				
STOP CARD -	+ S.E.N.D.						
	END CARD FOR DATA INITIA	AL ZATION		, 	<u> </u>		
	1 2 3 4 3 6 7 6 9 10 11 12 13 14	15 16 12 16 19 20 21 22 23 24 25 26 27 28 29	z en lan ka tantantantantantantantartartartartar dar ke terterlor	0'31'32'51'54'55'56'57'56 39 60 61 62 63 6	4165 6616 7166 691 701 7 11 72 17 11	74175176177178179160	

- 6. Request additional printout for SIMPAC data and for debug
- 7. Abort run
- 8. Node voltage solution tolerance
- 9. Battery charger load value and initiate charging
- 10. Change ac bus connections to inverters
- 11. Request circuit solution and formatted printout
- 12. Change ground reference node
- 13. Number of points in developing "OTHER" source I-V curve
- 14. "OTHER" source I-V curve points
- 15. Maximum time step increment

The variables that can be used in the card timeline are listed in Table 3-1. The deck setup for card timeline input is shown in Figure 3-9. Example card readout for timeline input is shown in Figure 3-10.

3.4 FORMATTED PRINTOUT DESCRIPTION

The formatted printouts available from SEPS Phase II are discussed in the subsequent paragraphs of this section and examples of each type of printed output are provided. The user can select or omit the Phase II printout by proper selection of the options described on the option card (Section 4.1).

3.4.1 DC Distribution Network Status

Figure 3-11 depicts the formatted printout for the Distribution Network parameters. The length of the printout will vary as a function of the number of branches in the circuit.

3.4.2 Source Status and Cryogenic Usage

Figure 3-12 depicts the formatted printout for source status, cryo status, and inverter status. The width of the printout will vary as a function of number of sources and inverters.

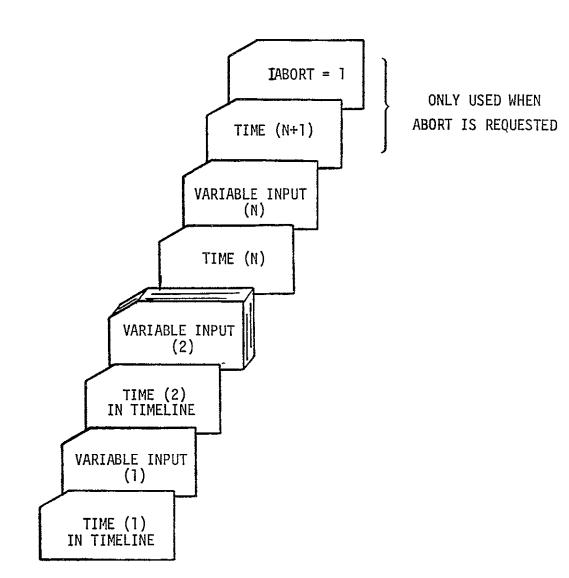


FIGURE 3-9'. CARD TIMELINE DECK SETUP

	DATE	PRIGRITY PROBLEM NO SPECIAL CHARACTERS:	TRW SYSTEMS HOUSTON COMPUTING CENTER SYMBOLIC AND FORTRAN	CARD STOCK PLAIN PERTHAN SOURCE	PAGE_ KEYPUNCHED BY_	OF
	EXT	1 1	CODING FORM	☐7094 SYMBOLIC	VERIFIED BY	
	S-190 L CPERATION	AGORESS YAG DECREMENT	COM	MENTS		SEQUENCE
	LINP BER 1+ FORTHAN STATEMENT	1 (6.17 6.11 6.120 2.11 221 21 24 25 26 27 28 29 20		2,531,54,55,56,57(58,59,60,61,63),63,64,0	15,86 67,68 68,70,75) 72,73,	74 75 76:7778 79 80
	1 1					
TIME POINT-	11, 0, 1				·············	
CARD	TIME IN THELINE FOR INSE	RTION OF DESIRED VARIABLES C		; <u> </u>	1 1 1 1 1 1 1	
	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	<u> </u>		1		 .
	111111111	<u> </u>	<u> </u>	 		
				i 	<u> </u>	
VARIABLE	╏╏╸╏╸╏╸╏╸╏╸╏╸	<u> </u>		<u> </u>		
CARD	\$ C.R.D.STT. 151(-81)=101	<u>, , , , , , , , , , , , , , , , , , , </u>	<u></u>	المسامل المسامل المبارك		
				; 	111111	111111
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	1111111111111	<u>i </u>		<u> </u>		
		VARIABLE VALUE				() () () () () ()
	VARIABLE TO	PE CHANCED	▗▗▊▗▐▗▐▗▐▗▐▗▐▗▐▗▐▗▐ ▘	_ <u>,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,</u>		
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	DENOTES: BEGINNING OF DATA	CHANGE FOR THAT TIME POINT		<u></u>		
	21.101 1 1 1 1 1 1 1 1		 	╌╏╴╏╶ ┞ _┄ ╏╌╂╌╂╌╂╌╂╌╂╌╂╌╂╌╂╌ ┆	<u> </u>	
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¥ ¥		1	1	1		
, 6	141-1612		 	<u>. </u>		<u> </u>
ў jj	\$1CIR.DIS T 1P1P1(121)1=1	3.7.5S.(121) = 11IRES	(ĔŗĨ;≓ 1; ; <mark>\$; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ;</mark>	 	<u> </u>	
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~ E	61.14141	<u> </u>	┖╶┩╒┋╒╃╸╂┈┨┈╏┈╏┈┩┉╏┉┞╌╏┈╇╼╄═╏┉┖┉┞┉┞┈┞ ┈	<u> </u>	┖┈┸┸┸┸┸	<u> </u>
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		<u> </u>	<u> </u>			
3 43	11 11 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1		; <u> 1 1 1 1 1 1 1 1 1</u>		
젊		<u> </u>	<u> </u>			
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	53-51 15 7010-H #FORTRAN STA	HENE IT CONTINUATION FIGURE 3-		NG _ EYAMPI F		•

FIGURE 3-10. CARD TIMELINE DATA LISTING - EXAMPLE

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FIGURE 3-11. DC DISTRIBUTION NETWORK STATUS

******	********	********	******	******	********	******	********	4 <i>6</i>	******	*******	*******
	- AY SSTO	NELAPSEO	TT4E - 155	.0000 TIME	STEP	. 2200 NE	T THEFT	ME 155.5	C40		
TOTAL SOURCE POWER	9032.0763	TOTAL DC/A	C LOAD 83	329.4440 RE	FERENCE NOD	E 27 ⁻ !	CCUR ATY	10010 S	DEUTIONS AT	TEMPTED	5
BRANCH SOURCE	STURCE	LOAD	LOAD PR	LOAD VOLTAGE	LOAD CURRENT_RE	LOAD SISTANCE	GRANCH CURFENT P	BRANCH ESISTANCE	VOLTAGE RE	DE ER RPÉ SISTANCE	SHUNT
RY SW VOLTAGE	CURRENT	. PP	L3	ACTI WOT	000000000000000000000000000000000000000					******	
************	********	*******	*******	******	****	*******	*******	*******	*****		
	102 22						102.2213	-0020			
1 1 31.23	102.22			-		_	67.2523	.0135			
i							12.3425	-0043			
1							22.6261	.0043			
ō				_	•		1700		.7000	.0040	:0056
<u> </u>							.0000	.0120			
. 0							:0000	.0120		•	**
0					=		14.4248	.0020	.7000	.0040	.0058
1					-		2:938T	- ₀0020	:700 o ~	- 0040	20057
1		E 0000	-0000	29.4099	.1700	172.9879	.1700	-0000			
3 10 1		5.0000 328.1180	.0000		10.8961	2.7637	10.8961	.0000			
9 1		.0000	.0000		.0000	INF.	.0000	.0000			,
8 1 7 1		509.2000	-0000	29.3268	17.3630	1.6890	17.3630	. 0000	-		
			1016.7063	30.1134	39.0516	-7711	39.0516	_0000			
7 47 1 3 6 1		80. 9020	.0000		2.6866	11.2088		.0000			
3 1		382.2228	.0000		12.3425	2.5091			_		. ,
- 		0000	0000		0000	INF.		-0000			
1 5 1		.0000	.0000		-0000	INF.	- 0000	:0000			
<u> </u>		- 240.4410	.0000	30.9240	" 7.T75Z	3:9772	7.7752 4.1258	.0020	.7009	.0040	.0059
3 1							5.1456	.0020	7000	.0040	0059
1					0.0711	3.0593		.0000	• 1000	••••	
		298-1000	0000	30-1992	9.8711	2.0273	103.2453	0020		· -	
$\frac{5}{5}$, $\frac{2}{2}$, $\frac{1}{1}$, $\frac{31.21}{1}$	103.25			_			71.3001	.0135			
<u> </u>							-14.8908				-
3 <u>1</u>							17.0460	.0043			
9 1							.0000	•0043			
0							.0000	-0120			
2 0		-		-		-	.0000	0120			-005
							0000	÷0020	.7000 T7000	PEVFPSED .0040	.0058
<u>4</u> <u>1</u>				'			6.7672	777.0020	7 1000	20040	*0050
8 20 1		5.0000	.0000		.0000	20+0000		.0000			
9 19 1		191.1180	.0000		6.3612	4.7231					
ó ia i		0000			.0000	INF.	.0000 6.7673	.0000			
i i i		200.0000	.0000		6.7673	4.3670		.0000			
2 43 1			1189-0340		45.5662 16.4174	1.8300		.0000			
3 16 1		493.2500	.0000	30.0444 30.9429	14.8988	2.0769		.0000			
4 13 1		461.0100		30.9429	.0000	INF.	.0000				
45 14 1 46 15 1		.0000			.0000	NF.	.0000				

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FIGURE 3-12. SOURCE AND INVERTER STATUS, AND CRYOGENIC USAGE	
FUEL CELL FUEL CELL AUX POWER AUX POWER AUX POWER AUX POWER	<u>.</u>
SHITCH CON 1 1 1 0 0 0 0	
CURRENT 102.22 103.25 83.32 .00 .00 .00 .00	
V1LTAGE 31.23 31.21 31.42 .00 .00 .00 .00 PAPASITIC .0000 .0000 .0000	
The second secon	
to a year to a superior and a superior to the	
BATTI BATTI BATTI	
TEMP 180.0000 180.0000 180.0000	
SDC AH.RFMAIN	
CRYOGEN USAGE LOADED REMAINING CONSUMED HZO PROD	
(LBS) (LBS) (LBS) (LBS)	
12. 800.00 -309.47 1109.47 1246.41	
H2 100.00 -41.78 141.78	
al and an annual of the contract of the contra	
INVERTER STATUS	
INVERTER 1 2 3	
AC TOAD 772.4000 908.0000 80.0000 POHFR FACTOR .9237 .9076 .9065	
MINEN EQUIUS 19231 47919 4799	

3.4.3 Constraint Analysis Printout

The Constraint violation printout as shown in Figure 3-13 will be provided at the end of the Phase II run if requested by the user.

3.4.4 Phase II COMUSE Printout

Phase II COMUSE Printout is the same as described for Phase I (Section 2.4) except that all power values are at the actual operating voltage rather than 28 volts.

3.5 PLOT TAPES

Phase II provides three separate plot tapes, selected at the option of the user (see 4.1 Program Option Cards), which can be used to plot selected Electrical Power System parameters as a function of mission elapsed time.

Table 3-2 defines the Shuttle plot parameters available while Table 3-3 defines the plot parameters available for ASTP CSM 111 and ASTP CSM 119.

3.6 PHASE II INTERFACE TAPE

Phase II of the SEPS program provides an interface tape, as an output, which is exactly like the Phase I interface tape (Section 2.6) except that power values are at the load operating voltages rather than at 28 volts dc. The Phase II interface tape is used as input to COMUSE to provide a load/subsystem mission phase analysis.

CONSTRAINT ANALYSIS

			_		
			TABLE DE	FINITION	B=. 401UE
TYPE		SCRIP			RELATIVE
1			ER VOLTA	CAPACITY	NODE Inverter
7			VERLOAD !		BRANCH
		INCH U			FUEL CELL
9	FUE				FUEL CELL
4	FUE				FUEL CELL
7	07	TANKS	DEPLETE	•	TANK
Ŕ	H2		DEPLETE		TANK
			SOC LESS	THAN LINIY	
•					· · · · · · · · · · · · · · · · · · ·
TIME	:	TYPE	RELATIVE	LIHIL	ACTUAL
			NUMBER	VALUE	AYFNE
	0.0	1	2	25.5000	25 - 15 [3
	00	1	5	25.5000	24.2840
	00	1	7	25.5000	24.4296
	00	1	9	25.5000	25.0734
	00	1	12	25.5000	24.3358 25.2125
	00	1	16 19	25.5000 25.5000	24.3846
-	60	1	21	25.5000	24.5125
	00	i	21	25.5000	23.9333
	80	i	26	25,5000	25.2125
	00	2	- 1	1250+0000	1955.5795
	מם	2	ż	1250.0000	1940.7242
	00	3 1	2	175.0000	245.4363
-	00	3	11	•0000	0000
	00	3	12	.0000	.0000
	00	Š	27	175+0000	249.6607
	00	3	3 &	•0000	.0000
	00	3	37	•0000	.0000
	OO.	3	52	175.0000	225.5228
	00	3	61	•0000	• 0000
	00	3	62	•900a	•0000
	00	3	84	250+0000	259.5504
	13	3	11	•0000	.0000
	. 13	3	12	•0000	•0000
	.13	3	36	.0000	•0000
	.13	3	37 61	+0000 +6000	0000 • 0000
	13	3	62	•8000	.0000
	33	3	ii	•6000	•0000
	. 33	3	12	•0000	0000
	33	3	36	.0000	.0000
	33	3	37	•000€	.0000
	. 33	3	61	•0000	• n0n0
	. 33	3	62	.0000	0000
	sn	3	11	*80.00	• 0000
	50	3	12	•0000	• 0000
	•50	3	36	•0000	• ՄՕՐԾ
	. 5 a	3	37	•0000	• n 8 9 0

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TABLE 3-2. PLOT PARAMETERS

LOCATION	QUANTITY	DEFINITION
1	1	Time
2-13	12	Source Voltages
14-25	12	Source Currents
26-37	12	Source Power
38	1	Total F/C Current
39	1	Total F/C Power
40	1	Accumulated F/C AMP-HR
41	1	Inverter No. 1 dc Load
42	1	Inverter No. 2 dc Load
43	1	Inverter No. 3 dc Load
44	1	Total Inverter dc Load
45	1	Accumulated Source KWH
46	1	02 Consumed
47	1	H2 Consumed
48	1	02 Remaining
49	1	H2 Remaining
50	1	H20 Produced
51-74	24	Bus (Node) Voltages

TABLE 3-3. ASTP PLOT PARAMETERS

The following are the word locations and word content for the SEPS ASTP PLOT TAPES generated for CSM 111 or CSM 119. The parameter values will be provided at each SEPS time point which includes all load changes.

WORD NO.	CSM 111 WORD CONTENT	WORD NO.	CSM 119 WORD CONTENT
1	Mission Time in Hours (GET)	1	Same as CSM 111
2	Total ac Power	2	Same as CSM 111
3	Total dc Power	3	Same as CSM 111
4-6	Power Out of Fuel Cells 1-3	4-5	Power Out of Fuel Cells 1-2
7	Total Fuel Cell Power	6	Same as CSM 111
8-10	Fuel Cell 1-3 Current	7-8	Fuel Cell 1-2 Current
11-13	Fuel Cell 1-3 Voltage	9-10	Fuel Cell 1-2 Voltage
14-16	Battery A,B,C Current	11-14	Battery A,B,C Descent Current
17-19	Battery A,B,C Voltage	15-18	Battery A,B,C, Descent Voltage
20-22	Battery A,B,C Ampere Hours	19-22	Battery A,B,C, Descent Ampere Hr
23-25	Fuel Cell 1-3 Temperature	23-24	Fuel Cell 1-2 Temperature
26	Total dc Energy Used	25	Same as CSM 111
27	Quantity H2 Remaining Tank 1	26	
28	Quantity H2 Remaining Tank 2	27	ļ
29	Quantity 02 Remaining Tank 1	28	
30	Quantity 02 Remaining Tank 2	29	
31	Total Fuel Cell Current	30	(
32	Total Battery Current	31	
33	Total Spacecraft Current	32	
34	Percent 02 Remaining Tank 1	33	
35	Percent 02 Remaining Tank 2	34	•
36	Percent H2 Remaining Tank 1	35	
37	Percent H2 Remaining Tank 2	36	
38	CM Main Bus A Voltage	37	
39	CM Main Bus B Voltage	38	
40-42	Fuel Cell (1-3) Internal Resistance	39-41	
43,44	Main Bus A &B Power	42-43	₹

4.0 PHASE I/PHASE II RUN PROCEDURES

4.0 PHASE I/PHASE II RUN PROCEDURES

4.1 PROGRAM OPTION CARDS

There are two option cards and one abort card that are used to define program operation. The first option card is used to request what type of analysis is to be performed and the output data to be obtained. The options are dependent on the "flags" set on this card. See Table 4-1 for option card format.

The second card is a "units" card. This card tells the program where the data required for Phase I and Phase II analysis can be located (i.e., circuit description, fixed data, etc.). The "units" card works in conjunction with where the user has assigned the tapes and the card deck inputs. The assignment location is based on the "1108 I/O UNITS" definition. The "units" card format is given in Table 4-2.

The abort card tells the program at what time to cease timeline analysis. The abort card format is given in Table 4-3. If the abort card is not used, the program will use an abort time of 500 hours.

Figure 4-1 shows a sample listing of the option card, units card, and abort card.

4.2 PHASE I DECK SETUP

4.2.1 Tape Assignments

The first cards to appear in the deck after the run card are those which assign the tapes necessary for a Phase I execution to appropriate tape units. Those tapes which must be assigned for Phase I are:

- 1. PCF Tape
- 2. SEPS Data Base Tape
- Working Tapes (or Fastrand) unit "O" (not zero) must be assigned and another for JVMMPS timeline
- 4. Tape for Dictionary and Interface

Other units can be assigned tapes to obtain optional analysis. E.g., Unit for Plot Tape - to obtain time versus power plot.

TABLE 4-1
OPTION CARD FORMAT

CARD		
COL		PURPOSE
1	> 0	Execute Phase I
	≈ 0 or blank	Do not execute Phase I
2	> 0	Execute Phase II
	= 0 or blank	Do not execute Phase II
3	> 0	Plot on Printer 1
,	= 0 or blank	Do not plot on Printer l
4	Not Used	*
5	> 0	Use 3 point load data
	= 0 or blank	Do not use 3 point load data
6 - 9	Not Used	
10	> 0	Print each PhaseII timepoint
	= 0 or blank	Do not print each Phase II
11 - 19	Not Used	Timepoint
20	> 0	Execute Phase II COMUSE
	= 0 or blank	Do not execute COMUSE
21 - 28	Not Used	
29	> 0	Suppress analysis part l
	= 0 or blank	Do not suppress analysis part l
30	> 0	Execute Phase I COMUSE
	= 0 or blank	Do not Execute Phase I COMUSE
31	> 0	Print Out Input Components
	= 0 or blank	No action taken

TABLE 4-1 (CONTINUED)

OPTION CARD FORMAT

CARD		
COL		PURPOSE
32	> 0	Print out input procedures
	= 0 or blank	No action taken
33	> 0	Print out input activities
	= 0 or blank	No action taken
34	> 0	Print out input timeline
	= 0 or blank	No action taken
35	> 0	Suppress COMUSE component analysis
	= 0 or blank	Do not suppress COMUSE comp. analysis
36	> 0	At the end of each mission phase Provide a Listing of all active components
	= 0 or blank	No action taken
37	> 0	Mission Phase analysis
	= 0 or blank	No Mission Phase analysis
38	> 0	Suppress cycled component listing
	= 0 or blank	Do not suppress cycled component listing,
39	> 0	Suppress print of compacted dictionary
	= 0 or blank	Do not suppress compacted dictionary printout
40	> 0	Suppress subsystem analysis at each time point
	= 0 or blank	Do not suppress subsystem analysis
i – 60	Not Used	

41

TABLE 4-1 (CONTINUED)

OPTION CARD FORMAT

CARD COL		PURPOSE
61-65	REAL NO.	Dead Band Width, Atl, for Phase I. All Timeline events which occur within the Interval from t to t + Atl will be grouped together and be made to occur at Time = t.
66-70	REAL NO.	$\Delta t2$ - maximum allowable time step Phase II. (HR)
71-75	INTEGER	Phase II print control - print out will be provided at every \$\Delta t p multiple of \$\Delta t 2\$
76-79	NOT USED	
80	INTEGER	Print Control Flag JPRINT <pre>< 1 Not Operative > 2 Print Initialization Data</pre>

TABLE 4-2

UNITS CARD FORMAT

COLUMNS 1 THROUGH 48 CONTAIN CERTAIN PROVISIONS TO DEFINE THE UNIT AND FILE FOR 12 DATA FILES. EACH DATA FILE REQUIRES FOUR COLUMNS. THE FIRST TWO ARE FOR THE UNIT NUMBER AND THE REMAINING TWO COLUMNS ARE FOR THE FILE NUMBER.

COLUMN	1-2	UNIT NUMBER FOR COMPONENTS
COLUMN	3-4	FILE NUMBER FOR COMPONENTS
COLUMN	5-6	UNIT NUMBER FOR PROCEDURES
COLUMN	7 - 8	FILE NUMBER FOR PROCEDURES
COLUMN	9-10	UNIT NUMBER FOR ACTIVITIES
COLUMN	11-12	FILE NUMBER FOR ACTIVITIES
COLUMN	13-14	UNIT NUMBER FOR TIMELINE
COLUMN	15-16	FILE NUMBER FOR TIMELINE
COLUMN	17-18	UNIT NUMBER FOR FIXED DATA
COLUMN	19-20	FILE NUMBER FOR FIXED DATA
COLUMN	21-22	NOT USED
COLUMN	23-24	NOT USED
COLUMN	25-26	NOT USED
COLUMN	27-28	NOT USED
COLUMN	29-30	UNIT NUMBER FOR INTERFACE PHASE I
COLUMN	31-32	FILE NUMBER FOR INTERFACE PHASE I

TABLE 4-2 (CONTINUED)

UNITS CARD FORMAT

COLUMNS 1 THROUGH 48 CONTAIN CERTAIN PROVISIONS TO DEFINE THE UNIT AND FILE FOR 12 DATA FILES. EACH DATA FILE REQUIRE FOUR COLUMNS. THE FIRST TWO ARE FOR THE UNIT NUMBER AND THE REMAINING TWO COLUMNS ARE FOR THE FILE NUMBER.

COLUMN	33-34	UNIT	NUMBER	FOR	PLOT PHASE I
COLUMN	35-36	FILE	NUMBER	FOR	PLOT PHASE I
COLUMN	37- 38	UNIT	NUMBER	FOR	INTERFACE PHASE II
COLUMN	39-40	FILE	NUMBER	FOR	INTERFACÉ PHASE II
COLUMN	41-42	UNIT	NUMBER	FOR	PLOT PHASE II
COLUMN	43-44	FILE	NUMBER	FOR	PLOT PHASE II
COLUMN	45-46	UNIT	NUMBER	FOR	DICTIONARY
COLUMN	47~48	FILE	NUMBER	FOR	DICTIONARY
COLUMN	49-50	UNIT	NUMBER	FOR	CIRCUIT DEFINITION
COLUMN	51-52	FILE	NUMBER	FOR	CIRCUIT DEFINITION
COLUMN	53-54	UNIT	NUMBER	FOR	CONSTRAINTS
COLUMN	55~56	FILE	NUMBER	FOR	CONSTRAINTS
COLUMN	57-58	UNIT	NUMBER	FOR	SUBSYSTEM DEF
COLUMN	59-60	FILE	NUMBER	FOR	SUBSYSTEM DEF
COLUMN	61-62	UNIT	NUMBER	FOR	MISSION PHASES
COLUMN	63-64	FILE	NUMBER	FOR	MISSION PHASES

TABLE 4-3

ABORT CARD

The format of the ABORT card is as follows:

COLUMN	PURPOSE
1-10	BLANK
11-20	ABORT TIME IN DECIMAL HOURS

The time given is that at which the processing of timeline is to terminate. If this card is omitted, the abort time is set to 500 hours.

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7	SI SIDE	}
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	DATE	PRIDRITYPROBLEM NO	TRW SYSTEMS HOUSTON COMPUTING CENTER	CARD STOCK	PASE CF
	SO OF CARDS	SPECIAL CHARACTERS	SYMEDLIC AND FORTRAN CODING FORM	Chouland Stinate	ERF (FD B)
	SYMUG. DEEPATION	ADDRESS TAG DECREMENT	COM	VENTS	SEGULNES
	THE PUR IN FORTPAN STATEMENT				
	1 2 2 2 4 4 5 6 7 2 9 (0, 11 12 1314 133	16,17,128,119,126,1731,221,231,24,125,26,271,281,29130,11 	1 *2 33 ⁺ 34 35 36 ⁺ 37 38 39 40 41 42 43 44 45 46 4748 4* 50 51 5	2 33 54 55 56 57 58 59 60 51 62 (63,64 65 65 67)	18 19 70 71 71 73 74 75 75 77 78 79 8
OPTION CARD		i 1 	<u> </u>	1	1 1 3
		} 6 1	!\ !	<u>}</u>	<u> </u>
	! FLAGS THAT! PHASE II RUN !		MAX. ALLOWA	BLE TIME STEP IN PHASE II	
	I IS REQUESTED	! •			PRINT CCHTROL (
	 	┞╸╏╸╏╸╏╸╏╸╏			
	11	.	·	PRINT CONTROL FLAG FOR INITIAL	
		┊╸ ┇╸┪┈┪╸┪╸┪╸┪╸┪╸┪	4-1-1-4-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1	ئىلىدىن يىلىنىڭ ئىلىلىدىنى بارىكىدىنى	marine de la companione
UNITS CARD		0230	050	li de autoriologia de la la la la la de la dec lara	
		FIXED DATA TAPE IS LOCATED	! TIMELI	NE IS LOCATED	· · · · · · · · · · · · · · · · · · ·
		ON UNIT 02 (B), FILE 30		D; DECK INPUT	
		1 04 0411 02 (5), FILE 30	ON CAN	<u> </u>	<u></u>
		; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ;	111111111111111111	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
ABORT CARD	9.0	ún	······································		
			┩┍┋┋┋┋┋┋┋┋┋┋┋┋┋┋	ļ. 1 - 1 1 - 1 1 1 1 1 1 1 1 1 1 1 1 1 1 	
		; <u> </u>	<u> </u>	i <u>,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,</u>	
		CARD ABORT TIME			
				<u> </u>	··· ··································
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4.2.2 CUR

The execution of program CUR at the beginning of a SEPS run serves to load the PCF tape into core,

4.2.3 WLCCIT

The execution of program WLCCIT is not a prerequisite to a SEPS execution. It is used only as a means of making changes to the SEPS data tape when necessary or when creating a new data tape. The changes are made file by file and in order as the files appear on the data tape. A new tape is made from input card files. Refer to Appendix G for a detailed explanation of WLCCIT.

4.2.4 JVMMPS

The execution of program JVMMPS creates a detailed mission timeline. It takes designated files from the data tape which contain timeline blocks (described in the timeline section) and puts them together in time sequence to form a mission timeline for Phase I analysis by SEPS. Refer to Appendix G for detailed explanation of JVMMPS.

4.2.5 SEPS

The first card following the "execute" card is an option card. The type of analysis performed and the output obtained depend on the "flags" set on this card.

E.g., I in Col 1 - execute Phase I analysis
I in Col 30 - execute COMUSE

The option card format is presented in Table 4-1.

The next card encountered is a "units" card. This card tells the program which tape unit and which file contain the data required for Phase I analysis. Units card format is given in Table 4-2.

The third card usually encountered is the mission abort card. It contains a single time at which the analysis must cease.

Mission phase cards, if used, are next encountered. These phases correspond to "phases" obtained from Mission documentation. They contain the following information:

- Card identifier "MM"
- 2. Dead band width if different from that on option card
- Mission Phase End in decimal hours
- 4. Mission Phase Title

The card format is given in Table 4-4. The data also can be included on the data tape as a separate file. These data are then input through the "units" card.

Appendices A, B, and C provide examples of Phase I run decks for 1) saving no output tapes, 2) using WLCCIT, and 3) saving interface and plot output tapes.

4.3 DECK SETUP FOR PHASE II RUN AND PHASE I/PHASE II RUNS

The deck setup shown in Figure 4-2 is used for all Phase II runs. The option and units cards direct the program in selecting the timeline mode (if interface tape is used or not used). The following subsections discuss the major building blocks of the deck setup and define what information is required

77

TABLE 4-4 MISSION PHASE CARD(S)

THE FORMAT OF THE MISSION PHASE CARDS IS AS FOLLOWS

CARD COLUMN PURPOSE		PURPOSE
1-2	MM	DEFINE THIS CARD AS A MISSION PHASE CARD
3-5	,	BLANK
6-10		DEAD BAND WIDTH, Δt_1 , PER THIS MISSION PHASE IF VALUE = 0 USE Δt_1 FROM OPTION CARD.
11-20		MISSION PHASE END TIME IN DECIMAL HOURS.
21-56		MISSION PHASE TITLE

THE TIME IS GIVEN AT WHICH THIS MISSION PHASE IS TO END. THE CARDS MUST BE IN ASCENDING TIME ORDER. THIS GROUP OF CARDS MUST BE TERMINATED BY A CARD WITH 'EE' IN CARD COLUMNS 1-2. THE PURPOSE OF THESE CARDS IS TO SEPARATE THE TIMELINE INTO DEFINED MISSION PHASES AND IS UTILIZED IN THE DEVELOPMENT OF THE COMUSE ANALYSIS.

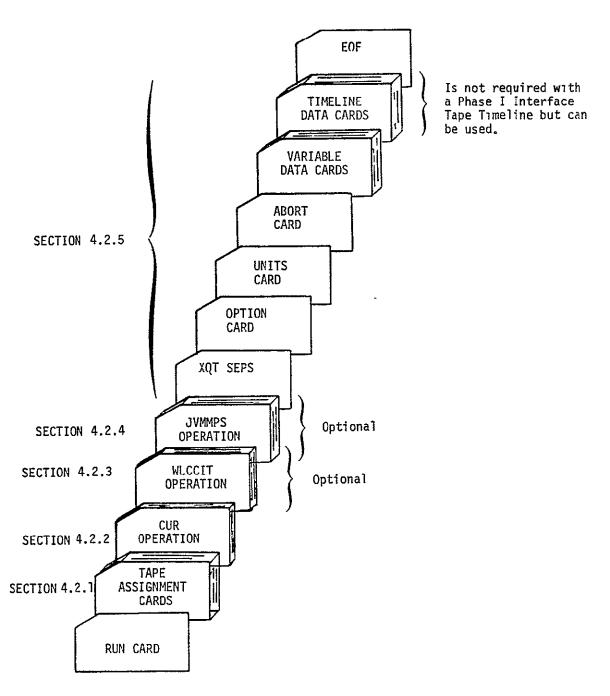


FIGURE 4-2 .

COMPLETE DECK SETUP FOR PHASE I/PHASE II RUN

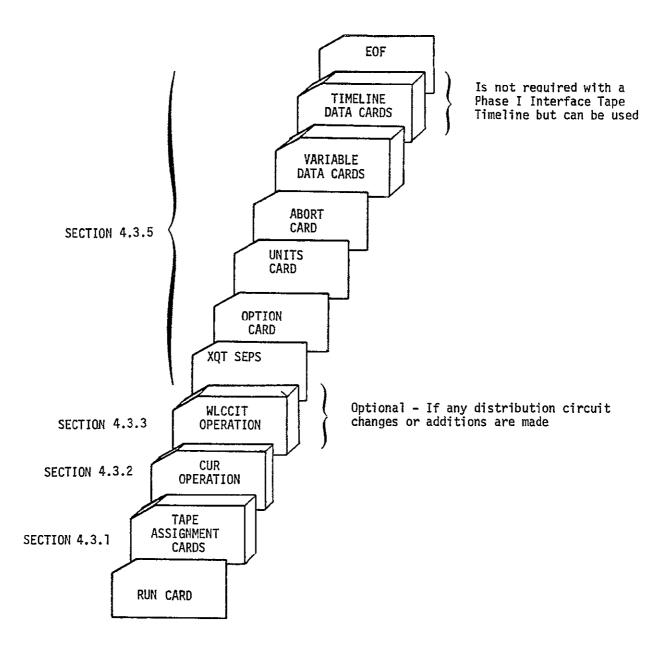


FIGURE 4-2 (CONTINUED)

COMPLETE DECK SETUP FOR PHASE II OR PHASE II
WITH PHASE I INTERFACE TAPE RUN

ORIGINAL PAGE IS OF POOR QUALITY in each block to conduct an interface tape timeline run or a card deck timeline run. To incorporate both the interface tape and card deck timeline inputs to be used in one run the interface tape deck setup with appropriate information is used with only the addition of the card deck timeline input.

4.3.1 Tape Assignments

The first cards to appear in the deck after the run card are those which assign the tapes necessary for a Phase II execution to appropriate units. The tapes which must be assigned for PHase II without interface tape are:

- 1. PCF Tape
- 2. SEPS Data Base Tape
- 3. Working tape unit "P" must be assigned to a work tape for Phase II

The tapes which must be assigned for Phase II with interface tape are:

- 1. PCF Tape
- 2. SEPS Data Tape
- 3. Working Tape unit "P"
- 4. Interface Tape with compacted dictionary

The tapes which must be assigned for Phase I/Phase II run are:

- 1. PCF Tape
- 2. SEPS Data Tape
- Working Tape unit "P" for Phase II (could be Fastrand)
- 4. Interface tape and compacted dictionary for Phase I output
- Working Tape Unit "O" for Phase I (could be Fastrand)
- JVVMPS (if used)
- 7. WLCCIT (if data changes are made to SEPS data tape)

Other units can be assigned tapes to obtain optional analysis:

- Phase II interface tape (required for Phase II COMUSE)
- 2. Plot tapes for Phase I and II
- 3. Constraints data storage

Figure 4-3 shows an example listing of the tape assignment cards.

4.3.2 CUR

The execution of CUR serves to load the PCF tape into core. Figure 4-3 shows an example of the cards listing CUR.

4.3.3 WLCCIT

The execution of program WLCCIT is not a prerequisite to a SEPS execution. It is used as a means of making changes or additions to the SEPS data tape. The changes are made file by file and in order as the files appear on the data tape. See Figure 4-4 for explanation of WLCCIT input cards. Additional information concerning WLCCIT is contained in Appendix G.

4.3.4 JVMMPS

The execution of program JVMMPS creates a detailed mission timeline. It takes designated files from the data tape which contain timeline blocks (described in the timeline section) and puts them together in time sequence to form a mission timeline for Phase I analysis by SEPS. The cards for JVMMPS would only be required if Phase I is run for developing an interface tape timeline. Additional information concerning JVMMPS is contained in Appendix G.

4.3.5 <u>SEPS</u>

The first three cards following XQT SEPS are the options, units and abort cards which are discussed in Section 4.1 and 4.2. Examples of the cards used for a card timeline run are shown in Figure 4-1. The next group of cards are for fixed data changes and data initialization. The data cards are discussed in Section 3.2 and example listing of the cards are shown in Figure 3-8. The final group of cards are the timeline cards. These cards are discussed in Section 3.3 and example listings of the cards are shown in Figure 3-10.

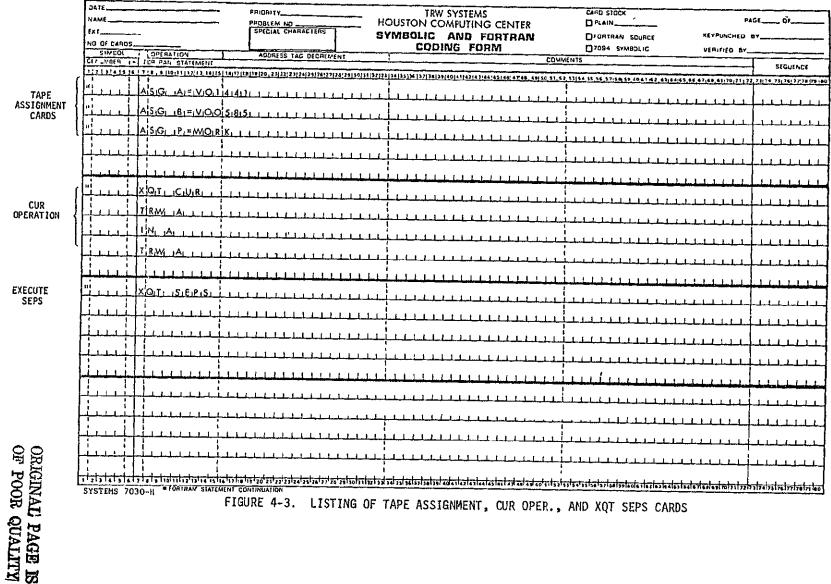
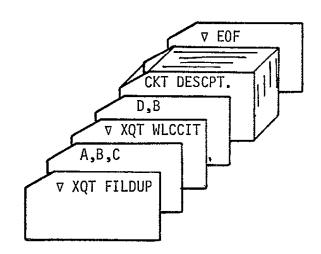


FIGURE 4-3. LISTING OF TAPE ASSIGNMENT, CUR OPER., AND XQT SEPS CARDS

FIGURE 4-4
BUILD NEW DATA FILE WITH CIRCUIT DESCRIPTION INPUT



Card 1: Execute FILDUP

Card 2: Units Card

A = Unit where data tape is located

B = Unit where new change tape is located

C = Is number of files to
 be copied on new tape.
 (new circuit will be
 stored in next file)

Card 3: Execute WLCCIT

Card 4: Units Card

D = Card reader input with circuit description data on cards

B = Unit where new change tape is located

Card 5 - N: Circuit Description

Cards

Card N + 1: End of File Card

Cards that define circuit

APPENDIX A

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A-1

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APPENDIX B

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APPENDIX C

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APPENDIX D

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APPENDIX E

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	XXXX PHASE I/PHASE IT BUN WITHOUT CARD TIMELINE	PRIGRITY	TRW SYSTEMS HOUSTON COMPUTING CENTER	CARD STOCK	PAGE 2 0F 2
	MAXX WITHOUT CARD TIMELINE	SPECIAL CHARACTERS	SYMBOLIC AND FORTRAN	DEORTHAN SOURCE	KEYPUNCHED BY
	NO DE CAROS	ADDRESS TAG DECREMENT	CODING FORM	* 7094 SYMBOLIC	VERIFIED BY
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APPENDIX F

HTTH MUR IT BLANKE

MXHX PHASE II RUN WITH CARD STOCK: TRW SYSTEMS PRIDRITY_ PAGE 2 01 2 PLAIN_ HOUSTON COMPUTING CENTER XYX INTERFACE TAPE AND CARD THEELINE INPUT (EXAMPLE) PROBLEM NO SPECIAL CHARACTERS KEYPUNCHED BY. SYMBOLIC AND FORTRAN FORTHAN SOURCE CODING FORM 7094 SYMBOLIC VERIFIED BY_ SYTULL | COPERATION ADDRESS TAG DECREMENT COMMENTS SEQUENCE Option Card-040204030404 Units Card Abort Card-Variable Data Changes and VARIABLE DATA CARA INPUT Initialization o Data Timeline Card Input TY MEKING CHAINGES, \$ SYSTEMS 7030-H *FORTHAN STATEMENT CONTINUATION

ä	XXXXPHASE	TYPHASE II RUN CARD TIMELINE	PRIORITY.		TRW SYSTEMS STON COMPUTING CENTER		CARD STOCK	PA	GE 1 OF 2
70	RYY	(EXAPPLE)	SPECIAL CHARACTERS		BOLIC AND FORTRAN		DEPARAN SOURCE	MEYPUNCHED &	ıv
	NO OF CAPOS.				CODING FORM		7084 SYMBOLIC	VERIFIED BY_	
	SYMBOL CENUMBER 14	FORTHAN STATEMENT	ADDRESS TAG DECREMENT			COMMEN	ITS		SEQUENCE
	11215121212	7 0 9 10 1 12 13 16 15	16[17]1#_1# 20 24 22 23 24 25 26 27 28,29	10[31]32[33]34]	5]36 37 38 39 40 44;42 43 44;45146 47;48 _, 49 3	30 51,32 51	2°54 55 56 57 514 59 6061 462 (63,44)	\$ 66:67 68 69:70 75 72	73,74 75 76 1776 19 60
Assign PCF	7'	ASG. A=1104	0,45	بليب	111111111111	بند	<u> </u>	, 	
Assign Data Tape	Vi i	ASG. B=1.0.4	4.6.7		1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		<u> </u>		
Assign Work Tape) For Phase I	<u> </u>	ASG O-WORK	<u>K </u>		11.111111111111	111	<u></u>		
Assign Work Tape For Phase II	7	ASG P		1.1.1		, , ,		111111	
Assign Interface Tabe I And	7	ASG C=WBR	K.1					4 1 1 1 1 1 1	
Dictionary Assign For Inter-	0	ASG D=VSA				1		- 	
face Tabe II	1	ASG EINOR				1	<u></u>		<u> </u>
Constraints Assign New Data	1	ASG FINSA			<u> </u>	- 	-1_1_1_1_1_1_1_1_1_1_1_1 _1_1	-1-1-1-1-1	
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Assign Timeline Tape	"	ASG GEWOR	«Burnini			<u></u>			
	1111	 				بب			
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	WAX PHASE I/PHASE II RUN WITH CARD TIMELINE	PRIDRITY	TRW SYSTEMS HOUSTON COMPUTING CENTER	DPLAIN	PAG	(_K_ 0/_Z)
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	SYMBOL DPERATION	ADDRESS TAG DECREMENT	COMM			SEQUENCE
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	SYSTEMS 7030-H *FORTRAN STATE		·			

APPENDIX G

CIFLIS

PURPOSE:

This program is used to list a given number of files from a card image tape.

USER GUIDE:

The input deck set-up consists of the execute card and a units card. The units card utilizes free field format, the first variable is the physical unit designation of where the tape is mounted and the second variable tells how many files are to be printed.

CMPDAT

PURPOSE:

This program is used to give a component and/or mission phase comparison of two interface tapes.

USER GUIDE:

The input deck set-up consists of the execute card, a units card, and a time frame definition card. Both the units card and the time frame definition card utilize fixed formats.

7

COMPARISON PROGRAM INPUT (CMPDAT)

Unit Definition Card

COLUMNS		PURPOSE
1-2	Unit	Standard Interface
3-4	File	·
5-6	Unit	Comparison Interface
7-8	File	
9-10	Unit	Component Dictionary
11-12	File	
13-14	Unit	Compact Dictionary
15-16	File	
17-18	Unit	Subsystem Names
19-20	File	•
21-22	Unit	Mission Phase Definitions
23-24	File	
25-30		Blank
31-40		Standard Inverter Efficiency
41-50		Comparison Inverter Efficiency
51-60		Standard Line Loss
61-70		Comparison Line Loss
71		Mission Phase Analysis Flag
Time Frame [Definition Card	
1-10		Standard Analysis Start Time
11-20		Standard Analysis End Time
21-30		Standard Analysis Delta Shift
31-40		Comparison Analysis Start Time
41-50		Comparison Analysis End Time
51-60		Comparison Analysis Delta Time

CREDJR

PURPOSE:

This program is used to make changes to the component definition card image tape file.

USER GUIDE:

The input deck set-up consists of the execute card, a units card, and a set of component change cards. The units card utilizes free field format and contains, first the input physical unit designation and secondly the output physical unit designation. The component change cards utilize a fixed format, obviously the same format as the component definition cards. The user inputs the component number, mode, and the new information in its correct field location. Any change to a field changes the complete field, i.e., it is not column or character replacement.

FILDUP

PURPOSE:

This program is used to duplicate card image files.

USER GUIDE:

The input deck set-up consists of the execute card and a set of units cards. The units cards utilize free field format. The first variable is the input physical unit designation, the second is the output physical unit designation, and the third is the number of files to be duplicated.

S. Carlo

JVMMPS

PURPOSE:

This program is used to concatenate several timeline files together and to time order the resulting file.

USER GUIDE:

The input deck set-up consists of the execute card, a units card, and a set of file definition cards. The units card utilizes free field format and contains the physical unit designation of the output unit. The file definition cards utilize a fixed format:

<u>Column</u>	Purpose				
1-2	Input physical unit designation				
3-4	File number				
5-9	Delta hours				
10-11	Delta minutes				
12-13	Delta seconds				

The delta times are used to shift the file times into the required time frame.

NEWHLP

PURPOSE:

This program is used to construct a tape able to be plotted showing user designated component's time history of operation.

USER GUIDE:

The input deck set-up consists of the execute card, a units card, and a set of component cards. The units card utilizes free field format and consists of eight variables:

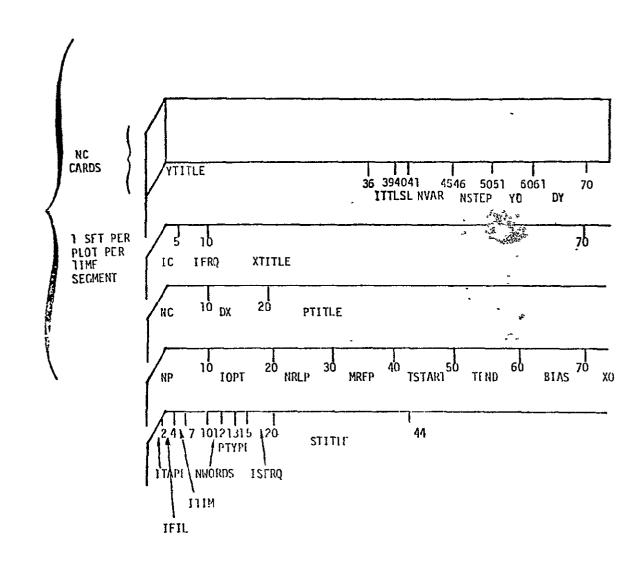
- 1 Physical unit designation of the compact component dictionary
- 2 File number
- 3 Physical unit designation of the timeline
- 4 File number
- 5 Physical unit designation of the plot tape
- 6 Print flag
 - = 0 Print time history of component operation
 - > 0 Suppress print
- 7 Analysis start time
- 8 Analysis stop time

The component cards utilize free field format and contain the component identification of the components to be analyzed.

STLPLT

PURPOSE:

Generate CalComp plots Mission Phase I or Phase II Plot tapes.



STLPLT CARD DECK SETUP

ORIGINAL PAGE IS OF POOR QUALITY

STLPLT CARD 1

CARD 1	(One Card Per Execution)				
COL JUSTIFICATION		VARIABLE	DESCRIPTION		
1-2 3-4 5-7 8-9	r r r	ITAPE IFIL ITIM	Input tape unit 'File no. Word location of time Blank		
10-12	r	NWORDS	Number of words per logical record		
13	r ,	PTYPE	Type of paper form plot is to appear on A- 10 inch by up to 41 inches of blank vellum B- 10 inch by up to 41 inches of continuous grid C- 7 inch by 10 inch of grid D- 10 inch by 15 inch of grid If no choice is made STLPLT assumes form Type B is to be used. Blank		
16-20	r	ISFRQ .	Standard frequency of symbol appearance ISFRQ>O symbol appearance every ISFRG th point ISFRQ=O symbol frequency of appearance every 20 th point ISFRQ <o be="" is="" made<="" plot="" point="" td="" to=""></o>		
21-44	L	STITL'E	Standard message to appear on all plots		

STUPLT CARD 2

			STLPLT CARD 2			
CARD 2	(One Card Per	(One Card Per Time Segment)				
COL	JUSTIFICATION	VARI ABLE	DESCRIPTION			
1-10	r	NP	Number of plots this time segment			
11-20	r	IOPT	Word location of the independent variable			
21-30	r	NREP*	Number of first repeated time segment to be plotted			
31-40	r	MREP*	Number of last repeated time segment to be ploited			
41-50	r	TSTART	Starting time of segment to be plotted			
51-60	r	TEND	Ending time of segment to be plotted			
61-70	r	BIAS	X-axis bias factor			
71-80	r	хо	Time to appear as start of the X-axis			
		* If	this option is not used, this value may be omitted			

CARD 3	(One Card Per P	lot)	STLPLT CARD 3
COL	JUSTIFICATION	VARIABLE	DESCRIPTION
1-10	r	ИС	Number of traces this plot
11-20	r	DX	X-axis scale delta factor per inch
21-80	L	PTITLE	Plot title
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		C. Ville Ville	, see

Card 4	(One Per Plot)	ST	LPLT CARD 4
	JUSTIFICATION	VARIABLE	DESCRIPTION
1-5	r	IC	Flag used, if value is nonzero, to override internal X-axis title
6-10	r	IFRQ	Symbol appearance frequency this plot if this value is zero standard frequency is used
11-70	٠.	XTITLE	X-axis title, if IC has a nonzero value this variable is used as the X-axis title
	,		
* *k			
i			

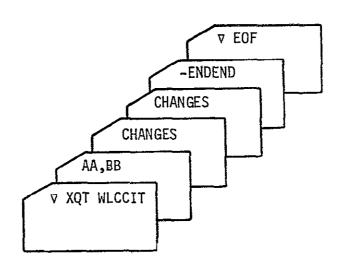
CARD 5	(One Card Per T	race)	STLPLT CARD 5	
COL	. JUSTIFICATION	VARIABLE	DESCRIPTION .	
7-36	L.	YTITLE	Y-axis title this trace	
37-38		,	Blank	
39-,40	r	ITTLSL	Y-axis title and scale suppress	
			ITTLSL = 0 no suppress = 1 suppress scale only = 2 suppress both scale and title	
41-45	· r	NVAR	Index of the variable to be plotted this trace	
46-50	r	NSTEP	Step function flag for this trace	
		· .	Value<0 indicates continuous trace Value>0 indicates step function trace	
51-60	r '	YO	Y-axis scale initial point	
. 61–70	r.	DY	Y-axis delta factor per inch	
			. For any negative or zero values the program will compute a Y-axis scale	
	•		·	
	•			
	•			

WLCCIT

PURPOSE:

This program is used to create or alter a card image tape file, as a by product the file is listed and each entry is numbered.

WLCCIT



Card 1: Execution Card

Card 2: Units Card

Col. 1-2: Unit where old tape to be changed is located or card reader unit

Col. 3: Comma

Card

Col. 4-5: Unit where new or changed tape is located

Card 3 N-2: Changes -

-X, Y Card for Line Change and/or data insertion

-X, Card for data insertion only

3: Line # where change is
to be made and/or after
which new data is to be
inserted. (Line # not
required when making new
tape from cards)

Card 4 N-1: New or Changed Data

Card N : -ENDEND

WLCCIT is the machine equivalent to physically removing data cards and replacing these cards with new data or simply adding new data.

Change Card E.g.'s:

To delete data:

Card 4: - 10, 15: Starting with

Line 10, delete
all information
up to & including
Line 15

To delete data and add data:

Card 4: - 10, 15 Followed by data to be inserted.

Deletes lines
10 15 inclusive and inserts new data.

To add data:

Card 4: - 10, Followed by data cards.

Inserts new data after line addressed on Card 1.